

# **Cypress Semiconductor Package Qualification Report**

**QTP# 014011 VERSION 1.1  
February, 2003**

**120-lead Thin Quad Flat Pack**

**14 x 14 x 1.4mm, MSL3**

**ANAM-SEOUL Korea Assembly**

## **CYPRESS TECHNICAL CONTACT FOR QUALIFICATION DATA:**

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**PACKAGE QUALIFICATION HISTORY**

<b>QUAL REPORT</b>	<b>DESCRIPTION OF QUALIFICATION PURPOSE</b>	<b>DATE COMP.</b>
014011	120-lead TQFP (14mm x 14mm x 1.4mm), die size 153.6 x 130.7, ANAM-SEOUL Korea Assembly, MSL3	Oct 01

<b>MAJOR PACKAGE INFORMATION USED IN THIS QUALIFICATION</b>	
<b>Package Designation:</b>	AT120
<b>Package Outline, Type, or Name:</b>	120-pin Thin Quad Flat Pack (TQFP)
<b>Mold Compound Name/Manufacturer:</b>	Sumitomo EME7351UL
<b>Mold Compound Flammability Rating:</b>	V-O per UL 94
<b>Oxygen Rating Index:</b>	> 28 %
<b>Lead Frame Material:</b>	Copper
<b>Lead Finish, Composition / Thickness:</b>	Solder Plated, 85 %Sn, 15 %Pb
<b>Die Backside Preparation Method/Metallization:</b>	N/A
<b>Die Separation Method:</b>	Wafer Saw
<b>Die Attach Supplier:</b>	Ablestik
<b>Die Attach Material:</b>	8361J
<b>Bond Diagram Designation</b>	10-03783
<b>Wire Bond Method:</b>	Thermosonic
<b>Wire Material/Size:</b>	1.0um
<b>Thermal Resistance Theta JA °C/W:</b>	45°C/W
<b>Package Cross Section Yes/No:</b>	N/A
<b>Assembly Process Flow:</b>	49-10044
<b>Name/Location of Assembly (prime) facility:</b>	Anam Seoul Korea (SEOL-L)

<b>ELECTRICAL TEST / FINISH DESCRIPTION</b>	
<b>Test Location:</b>	Anam Seoul Korea (SEOL-L)
<b>Fault Coverage:</b>	100 %

**Note:** Please contact a Cypress Representative for other packages availability.

**RELIABILITY TESTS PERFORMED PER SPECIFICATION REQUIREMENTS**

Stress/Test	Test Condition (Temp/Bias)	Result P/F
High Temperature Operating Life Early Failure	Dynamic Operating Condition, Vcc = 3.8V, 125°C	P
High Temperature Operating Life Latent Failure Rate	Dynamic Operating Condition, Vcc = 3.8V, 125°C	P
Temperature Cycle	MIL-STD-883C, Method 1010, Condition C, -65°C to 150°C Precondition: JESD22 Moisture Sensitivity MSL 3 192 Hrs., 30°C/60%RH+3IR-Reflow, 220°C+5, -0°C	P
Pressure Cooker	121°C, 100%RH Precondition: JESD22 Moisture Sensitivity MSL 3 192 Hrs, 30C/60%RH+3IR-Reflow, 220°C+5, 0°C	P
High Accelerated Saturation Test (HAST)	130°C, 3.63V, 85%RH Precondition: JESD22 Moisture Sensitivity MSL 3 192 Hrs, 30C/60%RH+3IR-Reflow, 220°C+5, 0°C	P
External Visual	Cypress Spec. 12-00292	P
Physical Dimensions	Cypress Spec. 25-00031	P
X-Ray	MIL-STD-883, Method 32012, Cypress Spec. 12-00292	P
Thermal Shock	-55C to +125C Cypress Spec. 25-00014	P
Acoustic Microscopy, MSL 3	Cypress Spec. 25-00104	P

\*This Qualification is contingent on the product CYS25G0101 qualification, QTP O11402.

### RELIABILITY FAILURE RATE SUMMARY

Stress/Test	Device Tested/ Device Hours	# Fails	Activation Energy	Acceleration Factor <sup>4</sup>	Failure Rate <sup>5</sup>
High Temperature Operating Life Early Failure Rate	1,047	0	N/A	N/A	0 PPM
High Temperature Operating Life <sup>1,2</sup> Long Term Failure Rate	368,184 HRs	0	0.7	170	45FIT

<sup>1</sup> Assuming an ambient temperature of 55°C and a junction temperature rise of 15°C.

<sup>2</sup> Chi-squared 60% estimations used to calculate the failure rate.

<sup>3</sup> Thermal Acceleration Factor is calculated from the Arrhenius equation

$$AF = \exp \left[ \frac{E_A}{k} \left[ \frac{1}{T_2} - \frac{1}{T_1} \right] \right]$$

where:

$E_A$  = The Activation Energy of the defect mechanism.

$k$  = Boltzmann's constant =  $8.62 \times 10^{-5}$  eV/Kelvin.

$T_1$  is the junction temperature of the device under stress and  $T_2$  is the junction temperature of the device at use conditions.

## Reliability Test Data

**QTP #: 014011**

<b>Device</b>	<b>Fab Lot #</b>	<b>Assy Lot #</b>	<b>Ass Loc</b>	<b>Duration</b>	<b>Samp</b>	<b>Rej</b>	<b>Failure Mechanism</b>
<b>STRESS: ACOUSTIC, MSL3</b>							
CYS25G0101GX-ATC (7B9532A)	4040862	610102176	SEOL-L	COMP	15	0	
CYS25G0101GX -ATC (7B9532A)	4048706	610111752	SEOL-L	COMP	15	0	
CYS25G0101GX -ATC (7B9532A)	4048679	610123195L1	SEOL-L	COMP	15	0	
<b>STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-EARLY FAILURE RATE, 125C, 3.8V, Vcc Max</b>							
CYS25G0101DX -ATC (7B9532A)	4048706	610118512L1	SEOL-L	96	1047	0	
<b>STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-LATENT FAILURE RATE (125C, 3.8V, Vcc Max)</b>							
CYS25G0101DX -ATC (7B9532A)	4048706	610118512L1	SEOL-L	168	379	0	
CYS25G0101DX -ATC (7B9532A)	4048706	610118512L1	SEOL-L	1000	366	0	
<b>STRESS: THERMAL SHOCK (+125C/-55C)</b>							
CYS25G0101GX -ATC (7B9532A)	4040862	610102176	SEOL-L	100	50	0	
CYS25G0101GX -ATC (7B9532A)	4040862	610102176	SEOL-L	200	50	0	
<b>STRESS: PHYSICAL DIMENSIONS</b>							
CYS25G0101GX -ATC (7B9532A)	4048706	610111752	SEOL-L	COMP	5	0	
<b>STRESS: EXTERNAL VISUAL</b>							
CYS25G0101GX -ATC (7B9532A)	4048706	610111752	SEOL-L	COMP	15	0	
<b>STRESS: X-RAY</b>							
CYS25G0101GX -ATC (7B9532A)	4048706	610121607	SEOL-L	COMP	15	0	
<b>STRESS: HI-ACCEL SATURATION TEST (130C, 85%RH, 3.63V), PRE COND 192 HR 30C/60%RH, MSL3</b>							
CYS25G0101GX -ATC (7B9532A)	4040862	610102176	SEOL-L	128	48	0	
<b>STRESS: PRESSURE COOKER TEST (121C, 100%RH), PRE COND 192 HR 30C/60%RH</b>							
CYS25G0101GX -ATC (7B9532A)	4040862	610102176	SEOL-L	168	47	0	

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<b>Device</b>	<b>Fab Lot #</b>	<b>Assy Lot #</b>	<b>Ass Loc</b>	<b>Duration</b>	<b>Samp</b>	<b>Rej</b>	<b>Failure Mechanism</b>
<b>STRESS: TC COND. C -65C TO 150C, PRECONDITION 192 HRS 30C/60%RH (MSL3)</b>							
CYS25G0101GX -ATC (7B9532A)	4040862	610102176	SEOL-L	300	46	0	
CYS25G0101GX -ATC (7B9532A)	4040862	610102176	SEOL-L	500	46	0	
CYS25G0101GX -ATC (7B9532A)	4040862	610102176	SEOL-L	1000	46	0	
CYS25G0101GX -ATC (7B9532A)	4048706	610118512L1	SEOL-L	300	60	0	
CYS25G0101GX -ATC (7B9532A)	4048706	610118512L1	SEOL-L	500	59	0	
CYS25G0101GX -ATC (7B9532A)	4048679	610123195L1	SEOL-L	300	59	0	
CYS25G0101GX -ATC (7B9532A)	4048679	610123195L1	SEOL-L	500	57	0	
CYS25G0101GX -ATC (7B9532A)	4048679	610123195L1	SEOL-L	1000	55	0	