Cypress Semiconductor
Product Qualification Report

QTP# 021508 VERSION 2.0
May 2004

CY5057WAF	**High-Frequency Flash Programmable PLL Die with Spread Spectrum**
CY25100	**Field- and Factory-Programmable Spread Spectrum Clock Generator for EMI Reduction**
S4AD-5 Technology, Fab 2	

CYPRESS TECHNICAL CONTACT FOR QUALIFICATION DATA:

Rene Rodgers
Principal Reliability Engineer
(408) 943-2732

PRODUCT QUALIFICATION HISTORY

Qual Report		Description of Qualification Purpose		Date Comp
010702		New Technology S4AD-5 / New Product, Programmable Clock Generator, CY2414ZC, its product family and bond option.		Apr 01
021508		CY5057WAF (7C800600) Device, S4AD-5 Technology		Jan 03
030905		CY25100 (7C80600A) Device package version of CY5057, S4AD-5 Technology		Mar 03

Cypress products are manufactured using qualified processes. The technology qualification for this product is referenced above and must be considered to get a complete and thorough evaluation of the reliability of the product.

PRODUCT DESCRIPTION (for qualification)	
Qualification Purpose: New CY5057WAF, CY21500 in S4AD-5 Technology in Fab 2	
Marketing Part #:	CY5057WAF, CY21500
Device Description:	3.3V Industrial and Commercial available via Die/Wafer sale, 8-pin SOIC/TSSOP
Cypress Division:	Cypress Semiconductor Corporation – Timing Technology Division (TTD) WA
Overall Die (or Mask) REV Level (pre-requisite for qualification):	Rev. A
What ID markings on Die:	7C80600A

TECHNOLOGY/FAB PROCESS DESCRIPTION S4AD-5			
Number of Metal Layers:	2	Metal Composition:	Metal 1: 500A Ti/6,000A Al 0.5% Cu /1,200A TiW Metal 2: 500A Ti/8,000A Al 0.5% Cu/300A TiW
Passivation Type and Materials:	3,000A TeOs / 6,000A Si_3N_4		
Free Phosphorus contents in top glass layer(%):	0%		
Number of Transistors in Device:	≈ 45,000		
Number of Gates in Device	≈ 45,000		
Generic Process Technology/Design Rule (μ-drawn):	Single Poly, Double Metal, 0.35 μm		
Gate Oxide Material/Thickness (MOS):	SiO_2 / 110A		
Name/Location of Die Fab (prime) Facility:	Cypress Semiconductor - Round Rock, TX		
Die Fab Line ID/Wafer Process ID:	Fab2, S4AD-5		

PACKAGE AVAILABILITY

PACKAGE	ASSEMBLY SITE FACILITY
8-pin SOIC	**Omedata, Indonesia (INDNS-O)**
8-pin TSSOP	**OSE-Taiwan (TAIWN-T)**

RELIABILITY TESTS PERFORMED PER SPECIFICATION REQUIREMENT

Stress/Test	Test Condition (Temp/Bias)	Result P/F
High Temperature Operating Life Early Failure Rate	Dynamic Operating Condition, Vcc Max=3.8V, 150°C	P
High Temperature Operating Life Latent Failure Rate	Dynamic Operating Condition, Vcc Max=3.8V, 150°C	P
High Accelerated Saturation Test (HAST)	130°C, 3.63V,85%RH Precondition: JESD22 Moisture Sensitivity MSL1 168 Hrs, 85C/85%RH+3IR-Reflow, 235°C+5, 0°C	P
Temperature Cycle	MIL-STD-883C, Method 1010, Condition C, -65°C to 150°C Precondition: JESD22 Moisture Sensitivity MSL1 168 Hrs, 85C/85%RH+3IR-Reflow, 235°C+5, 0°C	P
Pressure Cooker	121°C, 100%RH Precondition: JESD22 Moisture Sensitivity MSL1 168 Hrs, 85C/85%RH+3IR-Reflow, 235°C+5, 0°C	P
Data Retention	150°C ± 5°C No Bias	P
High Temperature Steady State life	150°C, 3.63V, Vcc Max	P
Electrostatic Discharge Human Body Model (ESD-HBM)	2,000V/ 2,200V MIL-STD-883, Method 3015.7	P
Electrostatic Discharge Charge Device Model (ESD-CDM)	500V Cypress Spec. 25-00020	P
Age Bond Strength	200C, 4hrs MIL-STD-883, Method 883-2011	P
Current Density	Cypress Spec 22-00029	P
Low Temperature Operating Life	-30C, 4.3V, 8MHZ	P
SEM Analysis	MIL-STD-883, Method 883-2018-2	P
Endurance Test	MIL-STD-883, Method 883-1033	P
Acoustic Microscopy, MSL1	Cypress Spec. 25-00104	P
Latchup Sensitivity	± 300mA In accordance with JEDEC 17. Cypress Spec. 01-00081	P

RELIABILITY FAILURE RATE SUMMARY

Stress/Test	Device Tested/ Device Hours	# Fails	Activation Energy	Thermal[3] A.F	Failure Rate[4]
High Temperature Operating Life Early Failure Rate[1]	4,481 Devices	1	N/A	N/A	223 PPM
High Temperature Operating Life[1,2] Long Term Failure Rate	238,000 DHRs	0	0 .7	170	23 FIT

[1] Assuming an ambient temperature of 55°C and a junction temperature rise of 15°C.
[2] Chi-squared 60% estimations used to calculate the failure rate.
[3] Thermal Acceleration Factor is calculated from the Arrhenius equation

$$AF = \exp\left[\frac{E_A}{k}\left[\frac{1}{T_2} - \frac{1}{T_1}\right]\right]$$

where:

E_A =The Activation Energy of the defect mechanism.
k = Boltzmann's constant = 8.62×10^{-5} eV/Kelvin.
T_1 is the junction temperature of the device under stress and T_2 is the junction temperature of the device at use conditions.

[4] **EFR Failure Rate and FIT Rate based on QTP #010702 and QTP #021508.**

Reliability Test Data

QTP #: 010702

Device	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
STRESS: ACOUSTIC-MSL1							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	COMP	15	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	COMP	15	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	COMP	15	0	
STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-EARLY FAILURE RATE, 150C, 3.8V, Vcc Max							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	48	1005	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	48	1004	1	NON VISUAL
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	48	1005	0	
STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-LATENT FAILURE RATE, 150C, 3.8V, Vcc Max							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	80	120	0	
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	500	120	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	80	120	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	500	120	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	80	120	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	500	120	0	
STRESS: AGE BOND STRENGTH							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	COMP	15	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	COMP	15	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	COMP	15	0	
STRESS: DYNAMIC LATCH-UP TESTING, 11.5V							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	COMP	3	0	
STRESS: LOW TEMPERATURE OPERATING LIFE, -30C, 4.3V							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	500	48	0	
STRESS: ESD-CHARGE DEVICE MODEL, 500V							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	COMP	9	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	COMP	9	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	COMP	9	0	

Reliability Test Data

QTP #: 010702

Device	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
STRESS: ESD-HUMAN BODY CIRCUIT PER MIL STD 883, METHOD 3015, 2,000V							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	COMP	9	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	COMP	9	0	
CY2414ZC (7C841400A)	2103764	*610106177*	TAIWN-T	COMP	10	0	
STRESS: STATIC LATCH-UP TESTING, 125C, 10V, ±300mA							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	COMP	3	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	COMP	3	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	COMP	3	0	
STRESS: HI-ACCEL SATURATION TEST, 130C, 85%RH, 3.63V, PRE COND 168 HR 85C/85%RH, MSL1							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	128	50	0	
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	256	50	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	128	48	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	128	48	0	
STRESS: HIGH TEMP STEADY STATE LIFE TEST, 150C, 3.63V							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	80	80	0	
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	168	80	0	
STRESS: ENDURANCE TEST							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	COMP	45	0	
STRESS: DATA RETENTION, PLASTIC, 150C							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	168	80	0	
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	552	80	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	168	80	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	552	80	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	168	80	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	552	80	0	
STRESS: PRESSURE COOKER TEST, 121C, 100%RH, PRE COND 168 HR 85C/85%RH, MSL1							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	168	50	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	168	49	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	168	51	0	

Reliability Test Data

QTP #: 010702

Device	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
STRESS: TC COND. C -65C TO 150C, PRECONDITION 168 HRS 85C/85%RH, MSL1							
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	300	50	0	
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	500	50	0	
CY2414ZC (7C841400A)	2101502	*610106170/1/2*	TAIWN-T	1000	50	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	300	50	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	500	50	0	
CY2414ZC (7C841400A)	2052404	*610106173/4/5*	TAIWN-T	1000	50	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	300	50	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	500	50	0	
CY2414ZC (7C841400A)	2103764	*610106176/7/8*	TAIWN-T	1000	49	0	

Reliability Test Data

QTP #: 021508

Device	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-EARLY FAILURE RATE, 150C, 3.8V, Vcc Max							
CY5057-SC (7C80600A)	2219267	*510206123*	INDNS-O	48	1467	0	
STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-LATENT FAILURE RATE, 150C, 2.3V, Vcc Max							
CY5057-SC (7C80600A)	2219267	*510206123*	INDNS-O	80	116	0	
CY5057-SC (7C80600A)	2219267	*510206123*	INDNS-O	500	116	0	
STRESS: ESD-CHARGE DEVICE MODEL, 500V							
CY5057-SC (7C80600A)	2219267	*510206123*	INDNS-O	COMP	9	0	
STRESS: ESD-HUMAN BODY CIRCUIT PER MIL STD 883, METHOD 3015, 2,200V							
CY5057-SC (7C80600A)	2219267	*510206123*	INDNS-O	COMP	9	0	
STRESS: STATIC LATCH-UP TESTING, 125C, 10V, ±300Ma							
CY5057-SC (7C80600A)	2219267	*510206123*	INDNS-O	COMP	3	0	