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Preface

Using the Product Safely

This guide contains important information about using this product safely. Be sure to read this guide before using the product and to follow the directions given to use the product correctly. In particular, read “Product Caution” and perform a thorough safety check before using the product.

Store this guide in a safe location where it can easily be accessed at any time while you are using the product.

Related Documents

- Traveo™ II Automotive Body Controller Entry Registers Technical Reference Manual (TRM) for CYT2B7, 002-19567
- Traveo™ II Automotive Body Controller Entry Registers Technical Reference Manual (TRM) for CYT2B9, 002-27181
- CYT2B7 Datasheet 32-bit Arm® Cortex®-M4F Microcontroller Traveo™ II Family, 002-18043
- CYT2B9 Datasheet 32-bit Arm® Cortex®-M4F Microcontroller Traveo™ II Family, 002-22825

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Target Product

The following product is the target for this evaluation board.

<table>
<thead>
<tr>
<th>Quantity</th>
<th>Description</th>
<th>Part No.</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Traveo II Starter Kit</td>
<td>CYTVII-B-E-1M-SK</td>
</tr>
</tbody>
</table>

Board Size

114 x 52 (mm).

Operating Temperature

0 to 40 °C.
RoHS Compliance

European RoHS.
1. Introduction

This guide provides directions for using the Traveo™ II Starter Kit, which is the evaluation environment for Traveo II Body Entry devices of the Cypress Traveo II family 32-bit microcontroller.

Table 1-1. Contents - Traveo II Starter Kit Board

<table>
<thead>
<tr>
<th>Quantity</th>
<th>Description</th>
<th>Part No.</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Traveo II Starter Kit Rev 3.0</td>
<td>CYTVII-B-E-1M-SK</td>
</tr>
</tbody>
</table>

Note: This user guide is meant for Rev 3.0 of the Traveo II Starter Kit.

1.1 Precautions and Warnings

The following precautions apply to the product described in this manual:

<table>
<thead>
<tr>
<th>Warning</th>
<th>Indicates a potentially hazardous situation which could result in death or serious injury and/or a fault in the user's system if the product is not used correctly.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Electric shock, Damage</td>
<td>Before performing any operation described in this manual, turn OFF all the power supplies to the system. Performing such an operation with the power ON may cause an electric shock or device fault. Once the product has been turned ON, do not touch any metal part of it. Doing so may cause an electric shock or device fault.</td>
</tr>
<tr>
<td>Caution</td>
<td>Indicates a hazardous situation that may cause a minor or moderate injury, damages to this product or devices connected to it, or loss of software resources and other properties such as data, if the device is not used appropriately.</td>
</tr>
<tr>
<td>Cuts, Damage</td>
<td>Before moving the product, be sure to turn OFF all the power supplies and unplug the cables. Watch your step when carrying the product. Do not use the product in an unstable location such as a place exposed to strong vibration or with slopes. Doing so may cause the product to fall, resulting in an injury or fault.</td>
</tr>
<tr>
<td>Damage</td>
<td>Do not place anything on the product or expose the product to physical shocks. Do not carry the product after the power has been turned ON. Doing so may cause a malfunction due to overloading or shock.</td>
</tr>
<tr>
<td></td>
<td>Since the product contains many electronic components, keep it away from direct sunlight, high temperature, and high humidity to prevent condensation. Do not use or store the product where it is exposed to much dust or a strong magnetic or electric field for an extended period.</td>
</tr>
<tr>
<td></td>
<td>Inappropriate operating or storage environments may cause a fault.</td>
</tr>
</tbody>
</table>
## Introduction

---

<table>
<thead>
<tr>
<th>Damage</th>
</tr>
</thead>
<tbody>
<tr>
<td>Use the product within the ranges given in the specifications.</td>
</tr>
<tr>
<td>Operation over the specified ranges may cause a fault.</td>
</tr>
<tr>
<td>To prevent electrostatic breakdown, do not let your finger or other object come in contact with the metal parts of any connectors. Before handling the product, touch a metal object (such as a door knob) to discharge any static electricity from your body.</td>
</tr>
<tr>
<td>When installing the sub board, align the key positions of main board connector and sub board connector, and fix with the screw set before use. When removing, remove all screws of screw set, and then remove the sub board vertically from the main board. Otherwise, the product may be damaged, for example, the connector section might be corrupted.</td>
</tr>
<tr>
<td>The product has no casing, so it is recommended that it be stored in the original packaging. Transporting the product may cause a damage or fault. Therefore, keep the packaging materials and use them when re-shipping the product.</td>
</tr>
</tbody>
</table>
2. Overview

This evaluation board provides developers with the evaluation environment of the CYT2B75/95 MCU.

Figure 2-1. Traveo II Starter Kit Board - Top View
2.1 Functional Overview

This board can mount the CYT2B75/95 series of MCU. The location of the main function is shown in Figure 2-2.

Figure 2-2. Traveo II Starter Kit Functional Overview
## Overview

<table>
<thead>
<tr>
<th>Item</th>
<th>Function</th>
<th>Specification</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>MCU</td>
<td>CYT2B75XX CYT2B95XX</td>
<td>U1: QFP 100-pin (0.5-mm pitch)</td>
<td></td>
</tr>
<tr>
<td>Oscillator (optional)</td>
<td>Main clock 16 MHz</td>
<td>X1: ABM10-16.000MHZ-D30-T3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Surface mounted main crystal</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>oscillator and load capacitances</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>(10 pF)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>OSC</td>
<td>X1: ABM10-16.000MHZ-D30-T3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>PSoc 5LP Kit-Prog3</td>
<td>CY8C5868LTI-LP039</td>
<td>U2: QFN 68-pin</td>
<td></td>
</tr>
<tr>
<td>Reset SW</td>
<td>System reset switch</td>
<td>SW2</td>
<td></td>
</tr>
<tr>
<td>User SW</td>
<td>User switch connects to MCU</td>
<td>SW1</td>
<td></td>
</tr>
<tr>
<td>Mode SW</td>
<td>Mode switch connects to PSoc</td>
<td>SW3</td>
<td></td>
</tr>
<tr>
<td>User LEDs</td>
<td>Two User LEDs connected to MCU</td>
<td>LED1 Blue</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>LED4 Blue</td>
<td></td>
</tr>
<tr>
<td>Power LED</td>
<td>Power monitor LED</td>
<td>LED2 Green</td>
<td></td>
</tr>
<tr>
<td>Status LED</td>
<td>KitPog3 status LED</td>
<td>LED3 Amber</td>
<td></td>
</tr>
<tr>
<td>USB connector</td>
<td>USB micro connector</td>
<td>J8: 10118194-0001LF</td>
<td></td>
</tr>
<tr>
<td>CAN FD interface</td>
<td>CAN FD channel</td>
<td>J5: CAN0_1 (Screw Terminal)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>CAN0_1</td>
<td>U3: TJA1057GT</td>
<td></td>
</tr>
<tr>
<td>Potentiometer</td>
<td>Potentiometer 10 kΩ</td>
<td>VR1: 3313J-1-103E</td>
<td></td>
</tr>
<tr>
<td>Arduino compatible pin headers</td>
<td>Header 2.54-mm pitch 8 x 1</td>
<td>J1, J2, J7, J9, J10</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Header 2.54-mm pitch 10 x 1</td>
<td>J11</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Header 2.54-mm pitch 18 x 2</td>
<td>J13</td>
<td></td>
</tr>
<tr>
<td>Debug connector (optional)</td>
<td>MIPI-10/20 Arm JTAG</td>
<td>J3: FTSH-110-01-L-DV-K</td>
<td>Not mounted by default</td>
</tr>
</tbody>
</table>
3. Operation Points and Locations

3.1 Power Supply

Power is supplied to this board from USB with a resettable fuse.

The current consumption can be measured at J4. J4 is not mounted by default, and instead R24 is mounted. To measure current consumption, remove R24 and mount J4.

Figure 3-1. MCU Power Supply Jumper Locations
### 3.2 Main Clock

The main clock (X1) location and details are shown in Figure 3-2 and Table 3-1. X1 is a 16-MHz oscillator with load capacitance.

**Figure 3-2. Main Clock Location**

<table>
<thead>
<tr>
<th>Part No.</th>
<th>Model Number</th>
<th>Remarks</th>
</tr>
</thead>
</table>
| X1       | ABM10-16.000MHZ-D30-T3 | ABRACON  
Surface-mounted 16-MHz oscillator |
3.3 Switches

This board has various switches for performing certain operations. The location and details of these switches are shown in Figure 3-3 and Table 3-2.

Figure 3-3. Switches Location

Table 3-2. Switches

<table>
<thead>
<tr>
<th>Part No.</th>
<th>Signal Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>SW1</td>
<td>User Switch: Connects to P7.0 of MCU</td>
</tr>
<tr>
<td>SW2</td>
<td>Reset Switch: System Reset</td>
</tr>
<tr>
<td>SW3</td>
<td>Mode Switch: Connects to P1[2] of PSoC 5LP</td>
</tr>
</tbody>
</table>
3.4 LEDs

This board has various LEDs for condition indication. The location and details of these LEDs are shown in Figure 3-4 and Table 3-3.

Figure 3-4. LED Location

Table 3-3. LEDs

<table>
<thead>
<tr>
<th>Part No.</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LED 1</td>
<td>User LED: Blue, Connects to P19.0 of DUT MCU</td>
</tr>
<tr>
<td>LED 2</td>
<td>Power LED: Green, Power monitor LED (VBUS)</td>
</tr>
<tr>
<td>LED 3</td>
<td>User LED: Amber, Connects to P1.4 of PSoC</td>
</tr>
<tr>
<td>LED 4</td>
<td>User LED: Blue, Connects to P12.2 of MCU</td>
</tr>
</tbody>
</table>
3.5 **USB Interface Connector**

This board has USB micro connector on the bottom side. SWD protocol is implemented on PSoC over USB and is connected to MCU. The location and details of USB micro connector are shown in Figure 3-5 and Table 3-4.

Figure 3-5. USB Interface Connector Location

![USB Interface Connector Location](image)

Table 3-4. USB Interface Connector

<table>
<thead>
<tr>
<th>Part No.</th>
<th>Description</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>J8</td>
<td>USB micro connector</td>
<td>FCI</td>
</tr>
<tr>
<td></td>
<td>10118194-0001LF</td>
<td>This connector is mounted on the bottom side</td>
</tr>
</tbody>
</table>
3.6 **CAN FD Interface Connector**

This board has a CAN FD interface. It is connected to a CAN FD channel of the DUT MCU.

The connector of CAN FD is 3-pin header. It is optional (not mounted by default). The CAN FD interface connector location and details are shown in Figure 3-6 and Table 3-5.

Figure 3-6. CAN FD Interface Connector Location

<table>
<thead>
<tr>
<th>Table 3-5. CAN FD Interface</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Part No.</strong></td>
</tr>
<tr>
<td>U3</td>
</tr>
<tr>
<td>J5</td>
</tr>
</tbody>
</table>
3.7 Potentiometer

This board has a potentiometer. It is connected to an ADC channel of the MCU.

The potentiometer location and details are shown in Figure 3-7 and Table 3-6.

Figure 3-7. Potentiometer Location

Table 3-6. Potentiometer

<table>
<thead>
<tr>
<th>Part No.</th>
<th>Description</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>VR1</td>
<td>Potentiometer 10 kΩ</td>
<td>BOURNS</td>
</tr>
<tr>
<td></td>
<td>3313J-1-103E</td>
<td>Connects to P6.0 (ADC[0]_0) of MCU</td>
</tr>
</tbody>
</table>
3.8 Arduino Compatible Pin Headers

This board has Arduino compatible pin headers to monitor MCU signals. The location and details of I/O headers are shown in Figure 3-8, Table 3-7, Table 3-8, Table 3-9, Table 3-10, Table 3-11, Table 3-12 and Table 3-13.

Figure 3-8. Arduino Compatible Pin Headers Location

| Table 3-7. Arduino Compatible Pin Header 1 (J1) |
|-------|---|
| Pin | Signal |
| 1 | NC |
| 2 | 5V |
| 3 | NC |
| 4 | NC |
| 5 | 5V |
| 6 | GND |
| 7 | GND |
| 8 | 5V |

| Table 3-8. Arduino Compatible Pin Header 2 (J2) |
|-------|---|
| Pin | Signal |
| 1 | A0 (P7_5) |
| 2 | A1 (P7_4) |
| 3 | A2 (P7_3) |
| 4 | A3 (P6_5) |
| 5 | A4 (P6_4) |
| 6 | A5 (P13_2) |
| 7 | A6 (P12_1) |
| 8 | A7 |
### Table 3-9. Arduino Compatible Pin Header 3 (J7)

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>A8 (P7_0)</td>
</tr>
<tr>
<td>2</td>
<td>A9 (P18_6)</td>
</tr>
<tr>
<td>3</td>
<td>A10 (P18_7)</td>
</tr>
<tr>
<td>4</td>
<td>NC</td>
</tr>
<tr>
<td>5</td>
<td>NC</td>
</tr>
<tr>
<td>6</td>
<td>NC</td>
</tr>
<tr>
<td>7</td>
<td>NC</td>
</tr>
<tr>
<td>8</td>
<td>NC</td>
</tr>
</tbody>
</table>

### Table 3-10. Arduino Compatible Pin Header 4 (J9)

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>14 (P18_1)</td>
</tr>
<tr>
<td>2</td>
<td>15 (P18_0)</td>
</tr>
<tr>
<td>3</td>
<td>16 (17_2)</td>
</tr>
<tr>
<td>4</td>
<td>17 (17_1)</td>
</tr>
<tr>
<td>5</td>
<td>18 (14_1)</td>
</tr>
<tr>
<td>6</td>
<td>19 (14_0)</td>
</tr>
<tr>
<td>7</td>
<td>20 (P0_3)</td>
</tr>
<tr>
<td>8</td>
<td>21 (P0_2)</td>
</tr>
</tbody>
</table>

### Table 3-11. Arduino Compatible Pin Header 5 (J10)

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>2 (P19_1)</td>
</tr>
<tr>
<td>4</td>
<td>3 (P19_2)</td>
</tr>
<tr>
<td>5</td>
<td>4 (P21_0)</td>
</tr>
<tr>
<td>6</td>
<td>5 (P21_1)</td>
</tr>
<tr>
<td>7</td>
<td>6 (P13_3)</td>
</tr>
<tr>
<td>8</td>
<td>7 (P13_5)</td>
</tr>
</tbody>
</table>
Table 3-12. Arduino Compatible Pin Header 6 (J11)

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>SCL</td>
</tr>
<tr>
<td>2</td>
<td>SDA</td>
</tr>
<tr>
<td>3</td>
<td>5V</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
</tr>
<tr>
<td>5</td>
<td>13 (P22_2)</td>
</tr>
<tr>
<td>6</td>
<td>12 (P22_0)</td>
</tr>
<tr>
<td>7</td>
<td>11 (P22_1)</td>
</tr>
<tr>
<td>8</td>
<td>10 (P22_3)</td>
</tr>
<tr>
<td>9</td>
<td>9 (P23_3)</td>
</tr>
<tr>
<td>10</td>
<td>8 (P21_5)</td>
</tr>
</tbody>
</table>

Table 3-13. Arduino Compatible Pin Header 7 (J13)

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>5V</td>
<td>2</td>
<td>5V</td>
</tr>
<tr>
<td>3</td>
<td>22 (P11_2)</td>
<td>4</td>
<td>23 (P11_1)</td>
</tr>
<tr>
<td>5</td>
<td>24 (P11_0)</td>
<td>6</td>
<td>25 (P8_2)</td>
</tr>
<tr>
<td>7</td>
<td>26 (P8_1)</td>
<td>8</td>
<td>27 (P18_2)</td>
</tr>
<tr>
<td>9</td>
<td>28 (P17_0)</td>
<td>10</td>
<td>29 (P14_3)</td>
</tr>
<tr>
<td>11</td>
<td>30 (P14_2)</td>
<td>12</td>
<td>31 (P13_7)</td>
</tr>
<tr>
<td>13</td>
<td>32 (P13_6)</td>
<td>14</td>
<td>33 (P13_4)</td>
</tr>
<tr>
<td>15</td>
<td>34 (P8_0)</td>
<td>16</td>
<td>35 (P3_0)</td>
</tr>
<tr>
<td>17</td>
<td>36 (P2_3)</td>
<td>18</td>
<td>37 (P12_4)</td>
</tr>
<tr>
<td>19</td>
<td>38 (P12_3)</td>
<td>20</td>
<td>39 (P12_2)</td>
</tr>
<tr>
<td>21</td>
<td>40 (P19_0)</td>
<td>22</td>
<td>41 (P3_1)</td>
</tr>
<tr>
<td>23</td>
<td>42 (P5_3)</td>
<td>24</td>
<td>43 (P2_0)</td>
</tr>
<tr>
<td>25</td>
<td>44 (P5_0)</td>
<td>26</td>
<td>45 (P5_1)</td>
</tr>
<tr>
<td>27</td>
<td>46 (P5_2)</td>
<td>28</td>
<td>47 (P18_3)</td>
</tr>
<tr>
<td>29</td>
<td>48 (P18_4)</td>
<td>30</td>
<td>49 (P18_5)</td>
</tr>
<tr>
<td>31</td>
<td>50 (P6_0)</td>
<td>32</td>
<td>51 (P6_1)</td>
</tr>
<tr>
<td>33</td>
<td>52 (P6_2)</td>
<td>34</td>
<td>53 (P6_3)</td>
</tr>
<tr>
<td>35</td>
<td>GND</td>
<td>36</td>
<td>GND</td>
</tr>
</tbody>
</table>
### 3.9 Debug Interface (Optional)

This board has a MIPI-10/20 (Arm standard) connector as the debugging interface. This connector is not mounted by default.

The debug connector location and details are shown in Table 3-14 and Table 3-15.

#### Table 3-14. Debug Interface Connector (J3)

<table>
<thead>
<tr>
<th>Part No.</th>
<th>Description</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>J3</td>
<td>MIPI-20 connector</td>
<td>SAMTEC</td>
</tr>
<tr>
<td></td>
<td>FTSH-110-01-L-DV-K</td>
<td>Not mounted by default</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(The foot pattern is available on the bottom side)</td>
</tr>
</tbody>
</table>

#### Table 3-15. MIPI-10/20 Connector

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Power</td>
<td>2</td>
<td>TMS</td>
</tr>
<tr>
<td>3</td>
<td>Ground</td>
<td>4</td>
<td>TCLK</td>
</tr>
<tr>
<td>5</td>
<td>Ground</td>
<td>6</td>
<td>TDO</td>
</tr>
<tr>
<td>7</td>
<td>NC</td>
<td>8</td>
<td>TDI</td>
</tr>
<tr>
<td>9</td>
<td>Ground</td>
<td>10</td>
<td>XRES</td>
</tr>
<tr>
<td>11</td>
<td>Ground</td>
<td>12</td>
<td>TRACE_CLOCK</td>
</tr>
<tr>
<td>13</td>
<td>Ground</td>
<td>14</td>
<td>TRACE_DATA_0</td>
</tr>
<tr>
<td>15</td>
<td>Ground</td>
<td>16</td>
<td>TRACE_DATA_1</td>
</tr>
<tr>
<td>17</td>
<td>Ground</td>
<td>18</td>
<td>TRACE_DATA_2</td>
</tr>
<tr>
<td>19</td>
<td>Ground</td>
<td>20</td>
<td>TRACE_DATA_3</td>
</tr>
</tbody>
</table>
4. Known Limitations

4.1 Known Limitations

This section lists the known limitations of the Traveo II Starter Kit.

Table 4-1. Limitation Information

<table>
<thead>
<tr>
<th>No.</th>
<th>Problem</th>
<th>Description</th>
<th>Workaround</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>CAN FD communication rate limit</td>
<td>The external oscillator is optional (Not mounted by default) and the CAN FD communication rate is 500 kbps at the maximum with IMO</td>
<td>To evaluate communication rates higher than that, install an external oscillator, or purchase an official evaluation board</td>
</tr>
</tbody>
</table>
A. Connections and Settings

A.1 Traveo II Starter Kit Connections

A.1.1 User LED

The correspondence between the LEDs on the board and the CYT2B75XX/CYT2B95XX device pins and the port pins as in Table A-1.

Table A-1. User LED

<table>
<thead>
<tr>
<th>User LED</th>
<th>Part Number</th>
<th>CYT2B75XX/CYT2B95XX</th>
</tr>
</thead>
<tbody>
<tr>
<td>USER LED</td>
<td>LED1</td>
<td>77</td>
</tr>
<tr>
<td>USER LED</td>
<td>LED4</td>
<td>47</td>
</tr>
</tbody>
</table>

A.1.2 User SW

The correspondence between the switch and the CYT2B75XX/CYT2B95XX device pin and the port pin as in Table A-2.

Table A-2. User Switch

<table>
<thead>
<tr>
<th>User SW</th>
<th>Part Number</th>
<th>CYT2B75XX/CYT2B95XX</th>
</tr>
</thead>
<tbody>
<tr>
<td>USER SW</td>
<td>SW1</td>
<td>29</td>
</tr>
</tbody>
</table>

A.1.3 RESET SW

The correspondence between the RESET SW and the CYT2B75XX/CYT2B95XX device pin and the port pin as in Table A-3.

Table A-3. RESET Switch

<table>
<thead>
<tr>
<th>RESET SW</th>
<th>Part Number</th>
<th>CYT2B75XX/CYT2B95XX</th>
</tr>
</thead>
<tbody>
<tr>
<td>RESET SW</td>
<td>SW2</td>
<td>85</td>
</tr>
</tbody>
</table>
A.1.4 MODE SW

The correspondence between the MODE SW and the PSoC 5LP pin number is given in Table A-4.

Table A-4. MODE Switch

<table>
<thead>
<tr>
<th>RESET SW</th>
<th>Part Number</th>
<th>PSoC 5LP</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Pin</td>
</tr>
<tr>
<td>RESET SW</td>
<td>SW3</td>
<td>13</td>
</tr>
</tbody>
</table>
B. Schematics and Layouts

B.1 Schematics
Figure B-2. Schematic (2/10)

Schematics and Layouts

Decoupling Capacitors

KitProg3 Status LED

Voltage Monitoring

UART Interface

I2C Interface

Mode Switch

USB Micro Connector

PSoc SLP Program/Debug Connector

Target Power

PSoc SLP Power
Figure B-3. Schematic (3/10)

**TVII Decaps, clock, reset, and debug**

**Figure B-3. Schematic (3/10)**

**TVII Decaps, clock, reset, and debug**

**CYP2B75 Decoupling Capacitors**

- **TV2 VDD**
  - C20 10uF/10V
  - C23 4.7uF/25V
- **TV2 VDD**
  - C21 10uF/16V
  - C24 10uF/16V
  - C25 10uF/10V
- **VDDIO**
  - C22 4.7uF/25V
  - C26 4.7uF/25V
  - C27 4.7uF/25V
  - C28 10uF/10V
- **VDDA**
  - C29 4.7uF/25V
  - C30 4.7uF/25V
- **VREFH**
  - C31 0.1uF/16V
  - C32 0.1uF/16V
  - C33 0.1uF/16V
  - C34 0.1uF/16V
  - C35 0.1uF/16V
  - C36 0.1uF/16V
  - C37 0.1uF/16V
  - C38 4.7uF/25V
  - C39 0.1uF/16V
  - C40 0.1uF/16V
  - C43 0.1uF/16V
  - C44 0.1uF/16V
  - C45 0.1uF/16V

**CYP2B75 Program/Debug Header**

- **VTARG**
  - SWJ_SWDIO_TMS
  - SWJ_SWDCLK_TSWJ
  - SWJ_SWO_TDO
  - SWJ_SWDIO_TMS
  - SWJ_SWDCLK_TSWJ
  - SWJ_SWO_TDO
  - SWJ_SWDIO_TMS
  - SWJ_SWDCLK_TSWJ
  - SWJ_SWO_TDO
  - SWJ_SWDIO_TMS
  - SWJ_SWDCLK_TSWJ
  - SWJ_SWO_TDO

**SWJ SWCLK_TCLK**

- **SWJ SWCLK_TCLK**
  - SWJ SWDOE_TDI
  - SWJ SWDOE_TDI
  - SWJ SWDOE_TDI
  - SWJ SWDOE_TDI
  - SWJ SWDOE_TDI

**16MHz crystal**

- **16MHz crystal**
  - X1 16.000MHz

**USER LED**

- **USER LED**
  - LED1

**RESET PUSH BUTTON**

- **RESET PUSH BUTTON**
  - R15

**USER PUSH BUTTON**

- **USER PUSH BUTTON**
  - P1_0_R

**KitProg3 connection**

- **KitProg3 connection**
  - (3) IDC_SCL
  - (3) IDC_SDA
  - UART_RX
  - UART_TX
  - SWDCLK
  - SWDIO

**CYP2B75 Jumper Current Measurement**

- **CYP2B75 Jumper Current Measurement**
  - R28 0 Ohm 1A
  - R29 0 Ohm 1A
  - R30 0 Ohm 1A
  - R31 0 Ohm 1A
  - R32 0 Ohm 1A
  - R33 0 Ohm 1A
  - R34 0 Ohm 1A
  - R35 0 Ohm 1A
  - R36 0 Ohm 1A
  - R37 0 Ohm 1A
  - R38 0 Ohm 1A

**Figure B-3. Schematic (3/10)**

**TVII Decaps, clock, reset, and debug**

- **TVII Decaps, clock, reset, and debug**
  - ECO_IN
  - ECO_OUT
  - I2C_SCL
  - I2C_SDA
  - UART_TX
  - UART_RX
  - RESET PUSH BUTTON
  - USER PUSH BUTTON
  - SWJ_SWDIO_TMS
  - SWJ_SWDCLK_TSWJ
  - SWJ_SWO_TDO
  - SWJ_SWDOE_TDI
  - SWDCLK
  - SWDOE_TDI
  - SWDIO
  - SWDOE_TDI
  - SWDCLK
  - SWDOE_TDI
  - SWDIO
  - SWDOE_TDI
  - SWDCLK
  - SWDOE_TDI
  - SWDIO
  - SWDOE_TDI
  - SWDCLK
  - SWDOE_TDI
  - SWDIO
  - SWDOE_TDI
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  - SWDIO
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  - SWDIO
  - SWDOE_TDI
  - SWDCLK
  - SWDOE_TDI
  - SWDIO
  - SWDOE_TDI
  - SWDCLK
  - SWDOE_TDI
  - SWDIO
Figure B-4. Schematic (4/10)

```
U1A

P0_0/PWM_18/PWM_22_N/TC_18_TR0/TC_22_TR1/SCB0_RX/SCB7_SDA/SCB0_MISO/LIN1_RX
P0_1/PWM_17/PWM_18_N/TC_17_TR0/TC_18_TR1/SCB0_TX/SCB7_SCL/SCB0_MOSI/LIN1_TX
P0_2/PWM_14/PWM_17_N/TC_14_TR0/TC_17_TR1/SCB0_RTS/SCB0_SCL/SCB0_CLK/LIN1_EN/CAN0_1_TX
P0_3/PWM_13/PWM_14_N/TC_13_TR0/TC_14_TR1/SCB0_CTS/SCB0_SDA/SCB0_SEL0/CAN0_1_RX
P2_0/PWM_7/PWM_8_N/TC_7_TR0/TC_8_TR1/SCB7_RX/SCB0_SEL1/SCB7_MISO/LIN0_RX/CAN0_0_TX/SWJ_TRSTN/TRIG_IN[2]
P2_1/PWM_6/PWM_7_N/TC_6_TR0/TC_7_TR1/SCB7_TX/SCB7_SDA/SCB0_SEL2/SCB7_MOSI/LIN0_TX/CAN0_0_RX/TRIG_IN[3]
P2_2/PWM_5/PWM_6_N/TC_6_TR0/TC_6_TR1/SCB7_RTS/SCB7_SCL/SCB2_SEL3/SCB7_CLK/LIN0_EN/TRIG_IN[4]
P2_3/PWM_4/PWM_5_N/TC_4_TR0/TC_5_TR1/SCB7_CTS/SCB7_SEL0/LIN5_RX/TRIG_IN[5]
P3_0/PWM_1/PWM_2_N/TC_1_TR0/TC_2_TR1/SCB6_RX/SCB6_MISO/TRIG_DBG[0]
P3_1/PWM_0/PWM_1_N/TC_1_TR0/TC_1_TR1/SCB6_TX/SCB6_SDA/SCB6_MOSI/TRIG_DBG[1]
P5_0/PWM_9/PWM_8_N/TC_9_TR0/TC_8_TR1/SCB5_SEL2/LIN7_RX
P5_1/PWM_10/PWM_9_N/TC_10_TR0/TC_9_TR1/LIN7_TX
P5_2/PWM_11/PWM_10_N/TC_11_TR0/TC_10_TR1/LIN7_EN
P5_3/PWM_12/PWM_11_N/TC_12_TR0/TC_11_TR1/LIN2_RX
P6_0/PWM_M_0/PWM_14_N/TC_14_M_0_TR0/TC_14_M_1_TR1/SCB4_RX/SCB4_MISO/LIN3_RX/ADC[0]_0
P6_1/PWM_0/PWM_M_0_N/TC_0_TR0/TC_0_TR1/SCB4_TX/SCB4_SDA/SCB4_MOSI/LIN3_TX/ADC[0]_1
P6_2/PWM_M_1/PWM_0_N/TC_1_M_1_TR0/TC_0_TR1/SCB4_RTS/SCB4_SCL/SCB4_CLK/LIN3_EN/CAN0_2_TX/ADC[0]_2
P6_3/PWM_M_1/PWM_1_N/TC_1_M_1_TR0/TC_1_M_1_TR1/SCB4_RTS/SCB4_SCL/SCB4_CLK/LIN3_EN/CAN0_2_RX/ADC[0]_3
P6_4/PWM_M_2/PWM_1_N/TC_2_M_2_TR0/TC_1_M_1_TR1/SCB4_SEL0/LIN4_RX/CAN0_2_RX/CAL_SUP_NZ/ADC[0]_4
P6_5/PWM_M_2/PWM_2_N/TC_2_M_2_TR0/TC_2_M_2_TR1/SCB4_SEL2/LIN4_EN/ADC[0]_5
P7_0/PWM_M_4/PWM_3_N/TC_3_M_4_TR0/TC_3_M_3_TR1/SCB5_RX/SCB5_MISO/LIN4_RX/ADC[0]_8
P7_1/PWM_M_4/PWM_4_N/TC_4_M_4_TR0/TC_4_M_4_TR1/SCB5_TX/SCB5_MOSI/LIN4_TX/ADC[0]_9
P7_2/PWM_M_5/PWM_5_N/TC_5_M_5_TR0/TC_5_M_5_TR1/SCB5_RTS/SCB5_SCL/SCB5_CLK/LIN4_EN/ADC[0]_10
P7_3/PWM_M_6/PWM_6_N/TC_6_M_6_TR0/TC_6_M_6_TR1/SCB5_SEL0/ADC[0]_11
P7_4/PWM_M_6/PWM_16_N/TC_16_M_6_TR0/TC_16_M_6_TR1/SCB5_SEL1/ADC[0]_12
```

Schematics and Layouts
Figure B-5. Schematic (5/10)

U1B

P7_5/PWM_17/PWM_M_6\_N/TC_17\_TR0/TC_M_6\_TR1/SCB5\_SEL2/ADC[0]\_13
P8_0/PWM_18/PWM_M_18\_N/TC_18\_TR0/TC_M_18\_TR1/LIN2\_RX/CAN0\_0\_TX
P8_1/PWM_20/PWM_M_19\_N/TC_20\_TR0/TC_M_20\_TR1/LIN2\_TX/CAN0\_0\_RX/TRIG\_IN[14]/ADC[0]\_16
P8_2/PWM_21/PWM_M_21\_N/TC_21\_TR0/TC_M_21\_TR1/LIN2\_EN/TRIG\_IN[15]/ADC[0]\_17
P11_0/ADC[0]\_M
P11_1/ADC[1]\_M
P11_2/ADC[2]\_M
P12_0/PWM_36/PWM_M_36\_N/TC_36\_TR0/TC_M_36\_TR1/CAN0\_2\_TX/TC\_17\_TR0/TC_M_17\_TR1/ADC[1]\_4
P12_1/PWM_37/PWM_M_37\_N/TC_37\_TR0/TC_M_37\_TR1/LIN6\_EN/CAN0\_2\_RX/TC\_19\_TR0/TC_M_19\_TR1/ADC[1]\_5
P12_2/PWM_38/PWM_M_38\_N/TC_38\_TR0/TC_M_38\_TR1/EXT\_MUX[1]\_EN/LIN6\_RX/ADC[1]\_6
P12_3/PWM_39/PWM_M_39\_N/TC_39\_TR0/TC_M_39\_TR1/EXT\_MUX[1]\_0/LIN6\_TX/ADC[1]\_7
P12_4/PWM_40/PWM_M_40\_N/TC_40\_TR0/TC_M_40\_TR1/EXT\_MUX[1]\_1/ADC[1]\_8
P13_0/PWM_M_8\_N/TC_M_8\_TR0/TC_M_43\_TR1/EXT\_MUX[2]\_0/SCB3\_RX/SCB3\_MISO/ADC[1]\_12
P13_1/PWM_44/PWM_M_9\_N/TC_M_9\_TR0/TC_M_44\_TR1/EXT\_MUX[2]\_1/SCB3\_TX/SCB3\_SDA/SCB3\_MOSI/ADC[1]\_13
P13_2/PWM_M_9\_N/TC_M_9\_TR0/TC_M_45\_TR1/EXT\_MUX[2]\_2/SCB3\_RST/SCB3\_SCL/SCB3\_CLK/ADC[1]\_14
P13_3/PWM_45/PWM_M_9\_N/TC_M_9\_TR0/TC_M_9\_TR1/EXT\_MUX[2]\_EN/SCB3\_CST/SCB3\_SEL0/ADC[1]\_15
P13_4/PWM_M_10\_N/TC_M_10\_TR0/TC_M_10\_TR1/SCB3\_SEL1/ADC[1]\_16
P13_5/PWM_46/PWM_M_10\_N/TC_M_10\_TR0/TC_M_10\_TR1/SCB3\_SEL2/ADC[1]\_17

CYT2B7
Figure B-6. Schematic (6/10)
Figure B-7. Schematic (7/10)

```
U1D

P19_1/PWM_26/PWM_M_3/N/TC_26_TR0/TC_M_3_TR1/TC_H_0/TR1/SCB2_TX/SCB2_SDA/SCB2_MOSI/FAULT_OUT_3
P19_2/PWM_27/PWM_26/N/TC_27/TR0/TC_26/TR1/TC_H_1/TR0/SCB2_RTS/SCB2_SCL/SCB2_CLK/TRIG_IN[28]
P19_3/PWM_28/PWM_27/N/TC_28/TR0/TC_27/TR1/TC_H_1/TR1/SCB2_CTS/SCB2_SEL0/TRIG_IN[29]
P21_0/PWM_42/PWM_43/N/TC_42/TR0/TC_43/TR1/SCB1_SEL2/WCO_IN
P21_1/PWM_41/PWM_42/N/TC_41/TR0/TC_42/TR1/WCO_OUT
P21_2/PWM_40/PWM_41/N/TC_40/TR0/TC_41/TR1/TRIG_DBG[1]/EXT_CLK/ECO_IN
P21_3/PWM_39/PWM_40/N/TC_39/TR0/TC_40/TR1/ECO_OUT
XRES
P21_5/PWM_37/PWM_38/N/TC_37/TR0/TC_38/TR1/LINO_RX
P22_0/PWM_34/PWM_35/N/TC_34/TR0/TC_35/TR1/SCB6_RX/SCB6_MISO/CAN1_1_TX/TRACE_DATA_0
P22_1/PWM_33/PWM_34/N/TC_33/TR0/TC_34/TR1/SCB6_TX/SCB6_SDA/SCB6_MOSI/CAN1_1_RX/TRACE_DATA_1
P22_2/PWM_32/PWM_33/N/TC_32/TR0/TC_33/TR1/SCB6_RTS/SCB6_SCL/SCB6_CLK/TRACE_DATA_2
P23_3/PWM_31/PWM_32/N/TC_31/TR0/TC_32/TR1/SCB6_CTS/SCB6_SEL0/TRACE_DATA_3
P23_5/PWM_24/PWM_25/N/TC_24/TR0/TC_25/TR1/SCB7_SEL2/SWJ_SWDIO_TMS
P23_6/PWM_23/PWM_24/N/TC_23/TR0/TC_24/TR1/SWJ_SWDIO_TMS
```

CYT2B7
Figure B-8. Schematic (8/10)
Figure B-9. Schematic (9/10)

Target Device
Figure B-10. Schematic (10/10)

**CANFD, Potentiometer**

**CAN Transceiver**

**Potentiometer**

- J19 JUMPER 1X2
- J20 JUMPER 1X2
- P0_2_SCB0_SCL
- P0_3_SCB0_SDA
- P12_2{6,10}
- J5 Screw terminal
- VR1 3313J-1-103E
- C47 0.1uF/16V
- R25 62 ohm
- R30 0 OHM RES0603
- TV2_VDD VR
- TV2_VDD_VR
- LED4 User LED BLUE
B.2 Layouts

Figure B-11. Layout (Top View)
Figure B-12. Layout (Bottom View)

SECONDARY ASSEMBLY
## Document Revision History

**Document Title:** Traveo II Starter Kit User Guide  
**Document Number:** 002-25314

<table>
<thead>
<tr>
<th>Revision</th>
<th>ECN#</th>
<th>Issue Date</th>
<th>Description of Change</th>
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<tbody>
<tr>
<td>**</td>
<td>6351876</td>
<td>10/16/2018</td>
<td>New User Guide</td>
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| *A       | 6934769 | 07/27/2020 | Added Note in Introduction.  
Replace **Figure 2-1** and **Figure 2-2**.  
Revised **Table 2-1**.  
Removed 2.1.1 Block Diagram because it is same as 2.1 Functional Overview.  
Replace **Figure 3-1 to Figure 3-8**.  
Revised **Table 3-1**, **Table 3-3**, and **Table 3-5**.  
Changed Section **3.8 Arduino Compatible Pin Headers**.  
Revised Section **3.9 Debug Interface (Optional)**.  
Revised **Table A-1**.  
Added Section **A.1.4 MODE SW**.  
Replace from "**Figure B-1 to B-4**" to "**Figure B-1 to Figure B-10**".  
Replace from "**Figure B-5 to B-6**" to "**Figure B-11 to Figure B-12**". |
| *B       | 6967760 | 09/18/2020 | Renamed "CYTVII-B-E-1M-SK Evaluation Board" to "Traveo II Starter Kit".                |