MEZZANINE CARD FOR TESTING THE HIGHEST PERFORMING MEMORY FOR SPACE APPLICATIONS

This Devkit board has been developed to work with the Cypress RadHard 72M & 144M QDR-II+ SRAMs. The board is developed and tested to work with FPGA carrier cards through the FMC-HPC connector.

CYDK-QDRII-NODT FEATURES

- 250 MHz Frequency
- 72/144 Mb Density
- x 18 or x 36 Data Bus Width
- Core VDD =1.8V (±0.1 V)
  I/O VDDQ = 1.4 V to VDD
- Socket for 165-pin CCGA (21 x 25 x 2.83 mm)

- FPGA mezzanine connector (FMC-HPC)
- Compatible with Microsemi-Microchip RTG4- DEV-KIT
- Compatible with Alpha-Data ADA-SDEV-KIT2

ADVANTAGES

- Supports all Cypress RadHard 72M & 144M QDRII+ SRAMs with onboard termination
- Only solution in the market that provides a FPGA mezzanine card supporting the full frequency of the Cypress RadHard QDRII+ SRAM when used with Cypress QDRII+ FPGA controller

FOR FURTHER DETAILS

To learn more about RH QDR-II+ and all Cypress RH products, visit http://www.cypress.com/aero

To purchase RH QDR-II+ parts, visit www.cypress.com/buyonline

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