CapSense Discrete Capacitors

Place each pair of caps close to Power Pins

Target PSoC Device

THIS IS FOR 1.8V OPERATION

Power Domain

UART Interface

I2C Interface

SPI Interface

SWD Programming Interface
The F-RAM FM24V10 on the CY8CKIT-041 Pioneer Kit is compatible with all I2C-based F-RAM devices from Cypress (FM24Vxx, FM24CLxx, and CY15BxxxJ parts). The F-RAM parts with more than 64 KB size support only four addresses (four devices of the same type on the same I2C bus); resistors connected to A1 (R68/R71) and A2 (R67/R70) pins can be used to select any of the four addresses. The F-RAM parts with less than 64 KB and FM24CLxx parts support eight addresses; resistors connected to A0 (R69/R72), A1 (R68/R71) and A2 (R67/R70) pins can be used to select one of the eight addresses.

**Memory Interface**

**Level Translator**

**F-RAM**

**CapSense Interface**

R75 is loaded by default to connect Shield pattern to PSoC 4100S GPIO (P3_5). Remove R75 and load R76 to connect Shield to GND.

**Battery Monitor**

**User Interface**

**Arduino Compatible Connectors**

**User Switch**

**Potentiometer**

Remove R118, while performing low power measurement.

R31(15K) is put in parallel with internal pullup (60K) to reduce effective delay in low power measurement.