Place each pair of caps close to Power Pins
The F-RAM FM24V10 on the CY8CKIT-048 is compatible with all I2C-based F-RAM devices from Cypress (FM24Vxx, FM24CLxx, and CY15BxxxJ parts). The F-RAM parts with more than 64 KB size support only four addresses (four devices of the same type on the same I2C bus); resistors connected to A1 (R44/R39) and A2 (R36/ R37) pins can be used to select any of the four addresses. The F-RAM parts with less than 64 KB and FM24CLxx parts support eight addresses; resistors connected to A0 (R46/R47), A1 (R44/R39) and A2 (36/ R37) pins can be used to select one of the eight addresses.
The diagram shows a circuit with various components including a Thermistor, Signal Processing Engine, Internal ADC, and interconnected lines with labels such as J2_P3_5, J2_P3_7, J6_P3_6, P3_5, P3_6, P3_7, and R4, R7, R83, R89, R141, R142, R86, and R87.

PCB: 600-60319-01
FAB DRW: 610-60294-01
ASSY DRW: 620-60302-01

CY8CKIT-048 PSoC Analog Co-Processor Pioneer Kit

CYPRESS SEMICONDUCTOR © 2016

Title: CY8CKIT-048 PSoC Analog Co-Processor Pioneer Kit

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L = Inductive Proximity Sensor

Sensor and Biasing circuit
Ambient Light Sensor

Transimpedance Amplifier (TIA)

Signal Processing Engine

Internal ADC
Passive circuit to bias the PIR signal to Vref

First stage amplifier
Gain is configurable

High Pass Filter to remove DC to bias to Vref gain

Signal Processing Engine

Internal ADC