# CY8CKIT-148 PSoC 4700S Inductive Sensing Evaluation Kit

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## Drawing Numbers

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SCH Title: CY8CKIT-148 PSoC 4700S Inductive Sensing Evaluation Kit

Page Title: Title Page

- Size: A4
- Document Number: 630-60503-01
- Drawn By: NMIT
- Approved By: RKAD
- Rev: 05
- Date: Wednesday, May 30, 2018
- Sheet: 1 of 7
PSoc 5LP based KitProg2

PSLP VDD

PSLP VCCD

SAR Bypass Capacitor

UART CTS

USB V SENSE

USB Type-C Connector

USB Type-C

Power and Status LEDs

Reverse Voltage Protection

3.3V Output Regulator

Decoupling Capacitors

Voltage Monitoring

Note: GND is read as binary "1" and floating as "0"

Note: Full down to disable SM flow control

Mode Switch

PSoc 5LP Power

PSLP VDD

R26 1 Ohm

VBUS

PSLP VDD

 UART Interface

UART CTS R3 100K

PSLP 0  Bit 0
PSLP 1  Bit 1
PSLP 2  Bit 2
PSLP 3  Bit 3
PSLP 4  Bit 4

ID = 0x05

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Page Title: KitProg2

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Sheet 3 of 7
Cylindrical Bumpons

Aluminium Overlay

USB Type C Cable

PCBA label

Metal Target

Accessories

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RKAD

05

Wednesday, May 30, 2018

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**REVISION HISTORY**