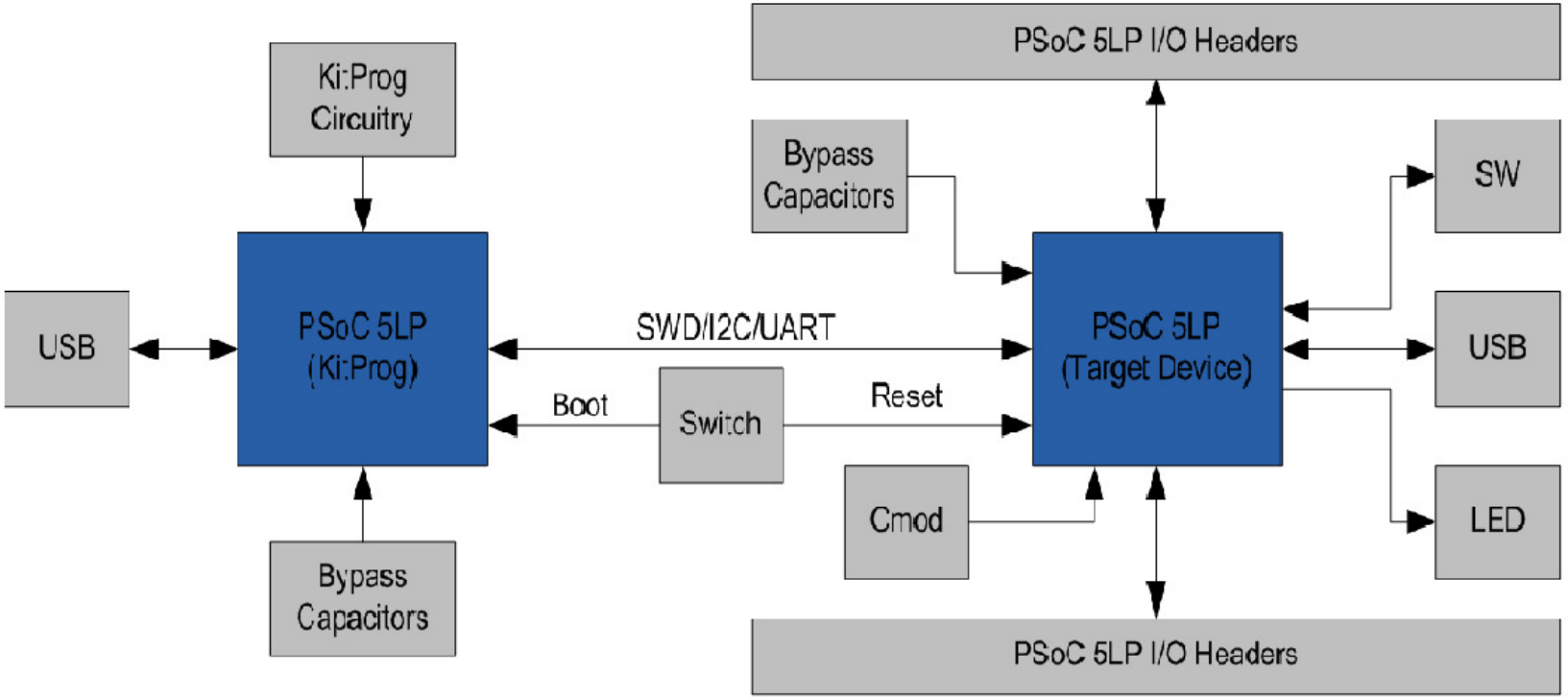

PCA:	121-60210-01
PCB:	600-60243-01
FAB DRW:	610-60235-01
ASSY DRW:	620-60243-01
SCH:	630-60242-01

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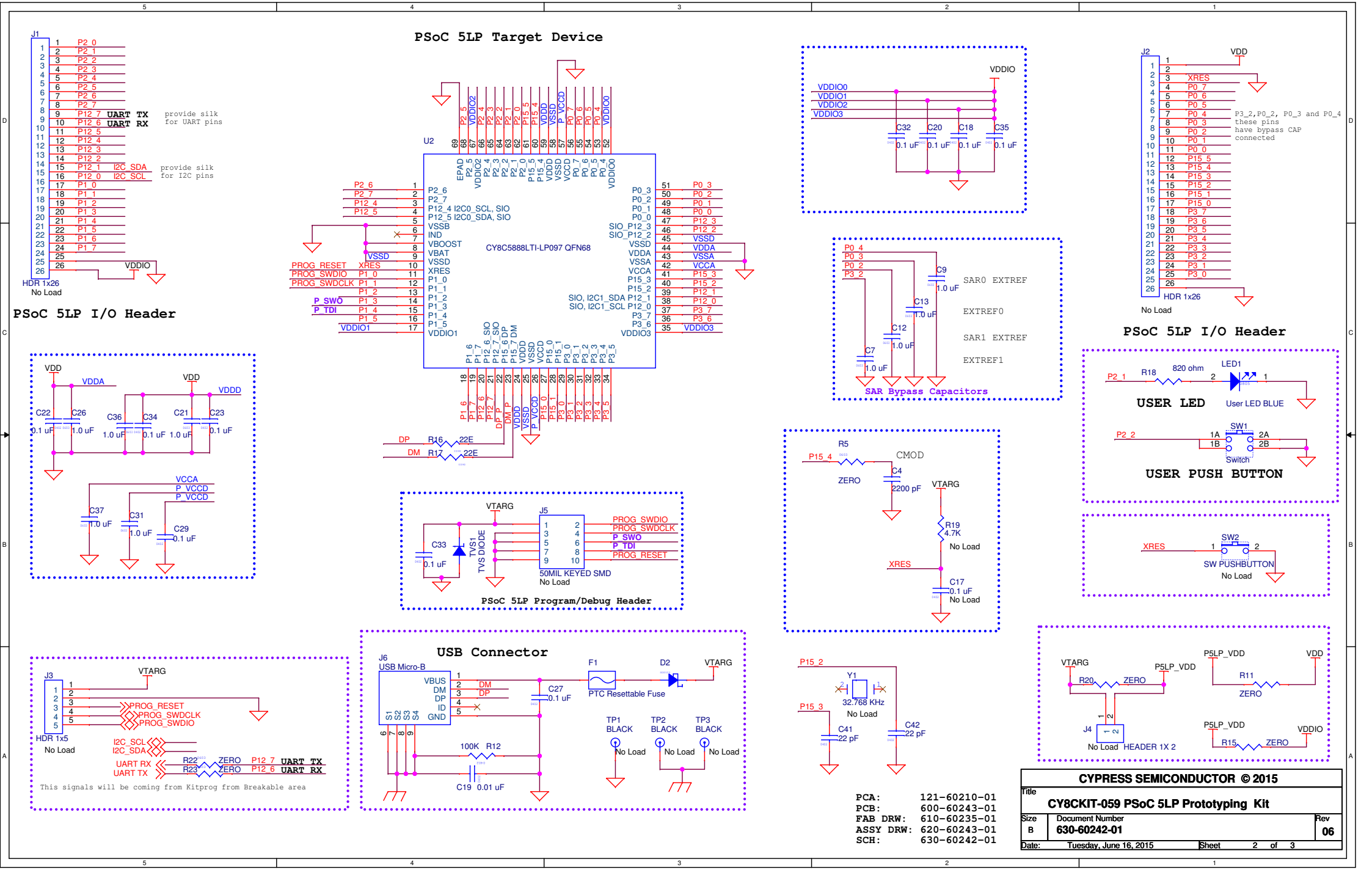
PSoC 5LP Target Device
PSoC 5LP I/O Header
PSoC 5LP I/O Header
PSoC 5LP Program/Debug Header
USB Connector
SAR Bypass Capacitors
USER LED
USER PUSH BUTTON
CY8C5888LTI-LP097 QFN68
HDR 1x26
No Load
provide silk for UART pins
provide silk for I2C pins
P3_2,P0_2, P0_3 and P0_4 these pins have bypass CAP connected
50MIL KEYED SMD
No Load
PTC Resettable Fuse
32.768 KHz
SW PUSHBUTTON
HEADER 1X 2
This signals will be coming from Kitprog from Breakable area
PCA: 121-60210-01
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ASSY DRW: 620-60243-01
SCH: 630-60242-01
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CY8CKIT-059 PSoC 5LP Prototyping Kit
Size B
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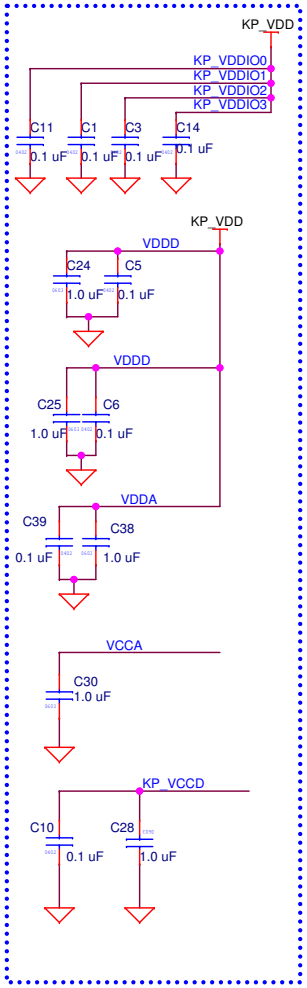
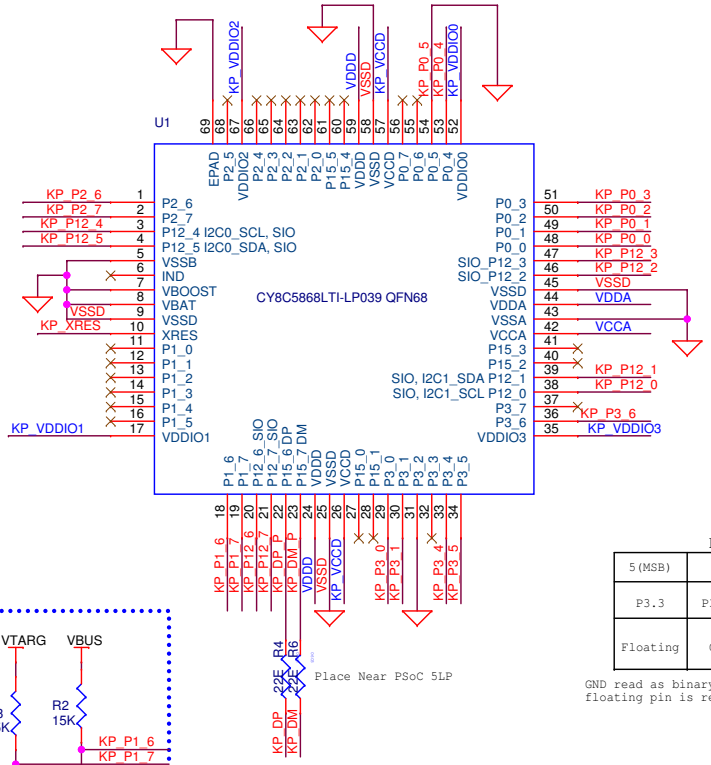
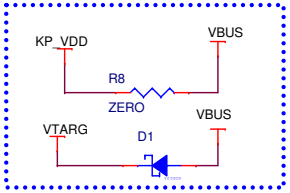
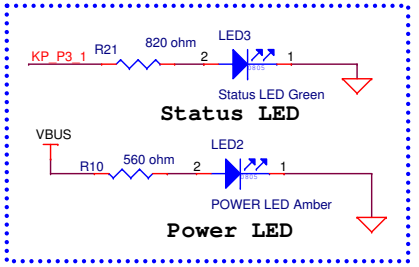
Sheet 2 of 3

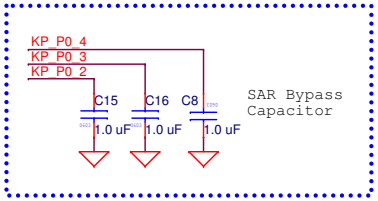
Kitprog on-board Programmer / Debugger

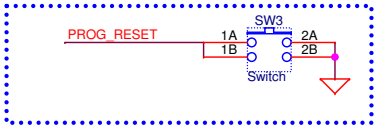








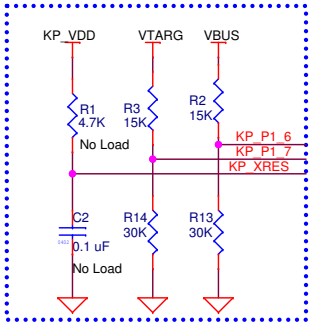


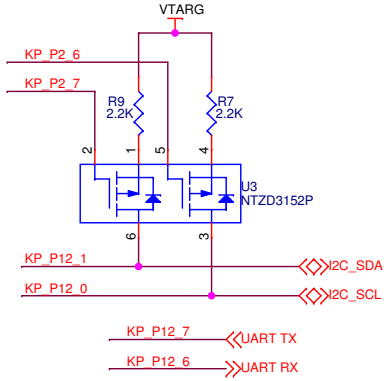


P3_3, P3_2, P0_7, P0_6, P0_5, P0_4 pins are reserved for HW REV ID

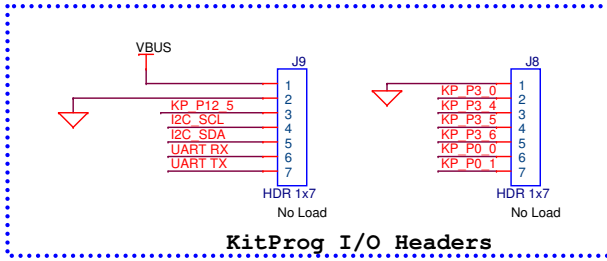
5(MSB)	4	3	2	1	0(LSB)
P3.3	P3.2	P0.7	P0.6	P0.5	P0.4
Floating	GND	Floating	Floating	GND	Floating

GND read as binary "1"
floating pin is read as binary "0"

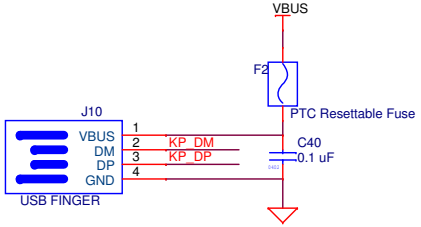




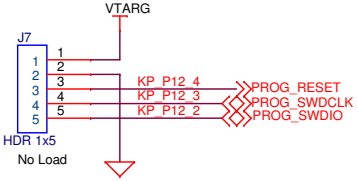
I2C & UART Connection



KitProg I/O Headers



USB Finger Connector



Target PSoC Program/Debug Header

PCA:	121-60210-01
PCB:	600-60243-01
FAB DRW:	610-60235-01
ASSY DRW:	620-60243-01
SCH:	630-60242-01

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