



Release Notes

CY3280-20x66 Universal CapSense® Controller Kit

Release Date: June 10, 2011

Thank you for your interest in the CY3280-20x66 Universal CapSense® Controller Kit. This document lists installation requirements, software and hardware updates, limitations, and known issues with the kit.

System Requirements and Recommendations

Hardware/Operating System Requirements	Minimum	Recommended
Processor Speed	1 GHz	2 GHz
RAM	1 GB	2 GB
Free Hard Drive Space	800 MB	1 GB
Screen Resolution	1024x768	1280x1024
CD/DVD Drive	✓	✓
USB	Full Speed	2.0 Hi-Speed
XP (SP2 or higher), Vista, or Windows 7	✓	✓
Software Prerequisites	Minimum	Recommended
Microsoft Internet Explorer	7	
Adobe Reader (for PDF documentation)	6	9+
Windows Installer	3.1	
.NET Framework	2.0 SP1	
PSoC Programmer	3.12	Latest*
PSoC Designer	5.1	Latest*

* Go to <http://www.cypress.com/go/CY3280-20x66> for the latest software downloads and documents.

Installation

To install, insert the kit CD into your PC's CD-ROM drive. If the installer does not start automatically, run *cyautorun.exe* in the root directory of the CD/DVD. Follow the installation instructions.

Note If you have a previous installation of PSoC Designer™ or PSoC Programmer, uninstall the same before reinstalling. To uninstall the software, go to **Start > Control Panel > Add or Remove Programs** and click the **Remove** button adjacent to the particular software. Follow the instructions to uninstall.

Kit Revision – New Feature for Rev. **

In this revision, the example projects are updated to support the latest version of PSoC Designer.



Limitations and Known Issues

- CapSense sensors are tuned for 3-mm overlay and can be extremely sensitive when used without overlay. If the CY3240-I2USB bridge board is not configured to supply power to the target device and does not detect power on the target device, then it will report an error when attempting to establish I2C communication. If no voltage is present on the VCC_PROG net, then the CY3240 I2USB bridge does not detect any power on the CY3280-20x66 board. This can occur if the power supply is from an external source, such as a wall transformer, and J1 is configured to supply power to the PSoC from either the 5 V or adjustable regulators.

The workaround is to configure the CY3240 I2USB bridge board to supply power to the target device, or modify the hardware to short the VCC net to the VCC_PROG net permanently.

- The silkscreen for pin 23 of connector P2 is misprinted as P3[1]. This is a no-connect (NC) pin and is therefore, not connected to any component on the board.

Documentation

Kit documents are located in the \Documentation folder in the default install location:

```
<Install_Directory>\Cypress\CY3280-20x66\<version>\Documentation
```

Documents include:

CY3280-20x66 Universal CapSense Controller Guide.pdf

CY3280-20x66 Universal CapSense Controller Quick Start.pdf

CY3280-20x66 Universal CapSense Controller Release Notes.pdf

Silicon Errata

To access the latest versions of the silicon errata, visit <http://www.cypress.com/psoc> and navigate to Errata.

Technical Support

For assistance, go to <http://www.cypress.com/go/support> or contact our customer support at +1(800) 541-4736 Ext. 8 (in the USA), or +1 (408) 943-2600 Ext. 8 (International).

Additional Information

- For more information about PSoC Designer functionality and releases, review the user guide and release notes on the PSoC Designer web page: <http://www.cypress.com/go/psocdesigner>
- For more information about PSoC Programmer, supported hardware, and COM layer, visit the PSoC Programmer web page: <http://www.cypress.com/go/psocprogrammer>
- For PSoC Designer training, visit <http://www.cypress.com/go/psocdesigner-training>



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