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Errata for the CY3280-20x66 Universal CapSense Controller Kit

This document describes the errata for the CY3280-20x66 kit. Details include errata trigger conditions, scope of impact, available workarounds, and kit revision applicability.

Contact your local Cypress Sales Representative if you have questions.

Part Numbers Affected

Part Number	Kit Revision Number
CY3280-20x66	**

CY3280-20x66 Qualification Status

Full Release

CY3280-20x66 Errata Summary

The following table defines the errata applicability to the CY3280-20x66 kit.

Items	Applicability	Fix Status
1. CY3240-I2USB Bridge Board Power Detection	Any projects using CY3280-20x66 kit hardware.	No hardware change planned.

1. CY3240-I2USB Bridge Board Power Detection

- **PROBLEM DEFINITION**
If the CY3240-I2USB bridge board is not configured to supply power to the target device and does not detect power on the target device, then it will report an error when attempting to establish I²C communication. If no voltage is present on the VCC_PROG net, then the I2C bridge will not detect any power on the CY3280-20x66 board. This can occur if power is supplied from an external source, such as a wall transformer, and J1 is configured to supply power to the PSoC from either the 5V or adjustable regulators.
- **PROJECTS AFFECTED**
Any projects using CY3280-20x66 kit hardware.
- **TRIGGER CONDITION(S)**
When the I²C-USB bridge board is configured to not supply power to the target device, and no voltage is present on the VCC_PROG net.
- **SCOPE OF IMPACT**
The I²C-USB bridge board will report an error because no power was detected coming from the CY3280-20x66 board.
- **WORKAROUND**
Configure the I²C-USB bridge board to supply power to the target device, or modify the hardware to permanently short the VCC net to the VCC_PROG net.
- **FIX STATUS**
No hardware change planned.

Document History Page

Document Title: CY3280-20x66 Kit Errata		
Rev.	Orig. of Change	Description of Change
**	GIR	Initial release of spec.

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