## CY8CKIT-062-WiFi-BT  PSoC 6 WiFi-BT Pioneer Kit

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PSoc 5LP based KitProg2

Decoupling Capacitors

Supply for PSoc 5LP

Note: Supply for PSoc 5LP (KitProg2) is enabled only when powered from KitProg2 USB port

I2C Interface

UART Interface

PSoc 5LP Status LEDs

PSoc 5LP User Switches

KitProg2 HW Revision

Note: GND is read as binary "1" and floating as "0"
**Decoupling Capacitors**

- C24: 10uF, 10V
- C18: 1uF, 50V
- C100: 1uF, 25V
- C104: 1uF, 50V
- C101: 1uF, 25V
- C105: 1uF, 25V
- C114: 1uF, 50V
- C26: 1uF, 10V
- C20: 0.1uF, 50V

**VDD Current Measurement**

- R148: 200mOhm, 1%
- R15: 0ohm
- J8: 2-pin 10A HDR, Default, loaded

**Voltage Follower**

Note: This circuit is to reduce the leakage current in VTARG domain

**PSoc 6 VDDUSB Enable**

- C16: 10uF, 50V
- C15: 0.1uF, 50V
- C14: 1uF, 25V
- C13: 1uF, 25V
- C12: 1uF, 50V
- C11: 1uF, 25V
- C10: 1uF, 25V
- C9: 1uF, 25V
- C8: 1uF, 25V
- C7: 1uF, 25V
- C6: 1uF, 25V
- C5: 1uF, 25V
- C4: 1uF, 25V
- C3: 1uF, 25V
- C2: 1uF, 25V
- C1: 1uF, 25V
- C0: 1uF, 25V

**PSoc 6 Power**

- Note: This circuit is to reduce the leakage current in VTARG domain

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**Page Title:** PSoc 6 Power

**Date:** Wednesday, January 29, 2020

**Sheet:** 9 of 16
**Accessories**

- **Cylindrical Bumper**
  - SJ61A4

- **Acrylic Overlay**
  - Acrylic Overlay
  - Color: Clear, Transparent
  - Finish: Matt
  - Size: 40 x 34 x 1mm

- **Jumper Shunt**
  - SPC02S{YAN}

- **USB Type-C Connector Brackets**
  - 298 SV001

- **PCBA label**
  - LBL PCA Label
  - LBL QR Code

**Jumper Shunt**

**PCBA label**

**USB Type-C Connector Brackets**

**Cylindrical Bumper**

**Acrylic Overlay**

**Jumper Shunt**
# REVISION HISTORY

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<tr>
<th>REV</th>
<th>DESCRIPTION OF CHANGE</th>
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<tr>
<td>01</td>
<td>Internal Release</td>
<td>2017/03/13</td>
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<td>02</td>
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<td>03</td>
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<td>2017/10/20</td>
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<td>04</td>
<td>Changed WCO load capacitors C85 and C86</td>
<td>2020/01/29</td>
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