

# Cypress Semiconductor Reliability Qualification Report

QTP# D22120f Version \*\*

## CYW20707VA2

**Qualification of CYW20707VA2, Single-Chip Dual Mode Bluetooth Transceiver / SoC for Embedded Wireless Devices in WLCSP (2.56 x 2.81 x 0.33mm) 36 Ball, Wafer Level Chip Scale Package (WLCSP)**

FOR ANY QUESTIONS ON THIS REPORT, PLEASE CONTACT  
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## I. Product and Package Information

**Product Description:** CYW20707VA2PKWBG      **Cypress Division:** IoT Division  
Single-Chip Dual Mode Bluetooth Transceiver / SoC for Embedded Wireless Devices

<b>Package:</b> WLCSP	<b>QTP:</b> D22120f	
<b>Description:</b> (2.56 x 2.81 x 0.33mm) 36 Ball, Wafer Level Chip Scale Package (WLCSP)		<b>Flammability: O2 Index:</b>
<b>Assembly:</b> PTI Taiwan	<b>Molding Compound:</b> N/A	UL-V0 >28
<b>Electrical Test:</b> ASE Taiwan	<b>Theta Ja / Psi Jt:</b> 34 °C/W / 1.3 °C/W	
<b>Substrate/Leadframe:</b> N/A	<b>Die Attachment:</b> N/A	
<b>Lead Finish:</b> 98.2Sn / 1.8Ag	<b>Bond Wire:</b> N/A	
<b>Comments:</b>		

<b>Est. Field Temperature:</b> 55 °C	<b>Life Test Temperature:</b> 125 °C
<b>Est. DC Field Current:</b> 40 mA	<b>Life Test Dynamic Current:</b> 10 mA
<b>Est. Field Voltage:</b> 3.3 V	<b>Life Test Voltage:</b> 3.6 V
<b>Est. Field Power Dissipation:</b> 132 mWatts	<b>Est. Stress Power Dissipation:</b> 36 mWatts
<b>Est. Field Tj:</b> 59.4 °C	<b>Est. Stress Tj:</b> 126.2 °C

<b>Die:</b> 20703A2A	<b>Die Size:</b> 2.56 x 2.81 mm
<b>Process:</b> 40NM LP	<b>Fab:</b> UMC-12A
<b>Type:</b> Bluetooth	<b>Density:</b> N/A

## II. 40nm GLL/LP/RF Life Test Failure Rate Calculation

### HTOL Stress Temperature - 125 °C

Failure Mechanism	Read Points / Test Results				Modeling Parameters @ 55°C					Avg. Failure Rate FITS @ 55°C, 60% Conf.	
	24 hrs	168 hrs	500 hrs	1000 hrs	Ea eV	TAF	VAF	OAF	MTTF (yrs)	PPM	FIT
<b>PLASTIC</b>											
Sample Size	4852	5238	3840	3746							
Zero fails, Process ave. Ea	0 *	0	0	0	0.7	71	1	71			
<b>Totals</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>					<b>38052</b>	<b>0</b>	<b>3</b>

\* - Contributes to early life FITS

### III. Summary of Stress Test Results

Stress Test	Stress Condition	Package Type	Sample Size	Num. of Lots	Num. of Fails	Failure Rate %	Comments
<b>Generic Reference Data:</b>							
<b>Early Life Failure Rate</b>	125°C, Vddnom x 1.15	FCFBGA <sup>1</sup>	96	1	0	0.00	24 Hours
<b>HTOL (EL)</b>	125°C, Vddnom x 1.15	FCFBGA <sup>1</sup>	96	1	0	0.00	192 Hours
<b>HTOL (IL)</b>	125°C, Vddnom x 1.15	FCFBGA <sup>1</sup>	96	1	0	0.00	500 Hours
<b>HTOL (XL)</b>	125°C, Vddnom x 1.15	FCFBGA <sup>1</sup>	96	1	0	0.00	1000 Hours
<b>High Temp Bake</b>	(150°C)	WLCSP <sup>2</sup>	77	1	0	0.00	1000 Hours
<b>ESD CDM</b>	N/A	FCFBGA <sup>1</sup>	3	1			Pass 500V
<b>ESD HBM</b>	N/A	FCFBGA <sup>1</sup>	3	1			Pass 2kV
<b>ESD MM</b>	N/A	FCFBGA <sup>1</sup>	3	1			Pass 150V
<b>Latch Up</b>	125°C	FCFBGA <sup>1</sup>	3	1			Pass 200mA
<b>Preconditioning</b>	(PC5/245°C, +0°C/-5°C)	WLCSP <sup>2</sup>	231	1			Passed Jedec L1
<b>Precon+Temp Cycle</b>	(PC5/245°C, -55°C/125°C)	WLCSP <sup>2</sup>	77	1	0	0.00	500 Cycles
	(PC5/245°C, -55°C/125°C)	WLCSP <sup>2</sup>	77	1	0	0.00	1000 Cycles
<b>Precon+uHAST</b>	(PC5/245°C, Unbiased, 130°C/85% RH)	WLCSP <sup>2</sup>	77	1	0	0.00	96 Hours
<b>Temp Humidity Test</b>	(Unbiased, 85°C/85% RH)	WLCSP <sup>2</sup>	77	1	0	0.00	500 hours
	(Unbiased, 85°C/85% RH)	WLCSP <sup>2</sup>	77	1	0	0.00	300 hours
	(Unbiased, 85°C/85% RH)	WLCSP <sup>2</sup>	77	1	0	0.00	1000 hours

**Notes / Justification:** 1) Results from Qual D22120a, BCM20704UA1KFFB in 49 Ball FCFBGA (4.5 x 4 x 1mm) - Similar Product and Die Size, Same Process Technology  
 2) Results from Qual PQ03168, BCM43907KWBG in 316 Ball WLCSP (4.907 x 5.848 x 0.33mm) - Same Assembly Facility and Process Technology

**Preconditioning Flows:** PC5 (JEDEC L1): Bake 125°C, 24hr => Soak @ 85°C/85%RH, 168hr => 3x Reflow

### Reliability Tests Performed per Specification Requirements

Stress	Condition	Specification Reference
Early Life Failure Rate	125°C, Vddnom x 1.15	JESD22-A108 / AEC-Q100-008
ESD CDM	N/A	JS002 / AEC-Q100-011
ESD HBM	N/A	JS001 / AEC-Q100-002
ESD MM	N/A	JS001 / AEC-Q100-002
High Temp Bake	(150°C)	JESD22-A103
HTOL (EL)	125°C, Vddnom x 1.15	JESD22-A108
HTOL (IL)	125°C, Vddnom x 1.15	JESD22-A108
HTOL (XL)	125°C, Vddnom x 1.15	JESD22-A108
Latch Up	125°C	JESD78 / AEC Q100-004
Precon+Temp Cycle	(PC5/245°C, -55°C/125°C)	JESD22-A104
Precon+uHAST	(PC5/245°C, Unbiased, 130°C/85% RH)	JESD22-A118
Preconditioning	(PC5/245°C, +0°C/-5°C)	J-STD-020
Temp Humidity Test	(Unbiased, 85°C/85% RH)	JESD22-A101

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## IV. Revision History

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**Document Number:** 002-22379**Document Title:** QTP #D22120f: CYW20707VA2, Single-Chip Dual Mode Bluetooth Transceiver / SoC for Embedded Wireless Devices

Rev.	Issue Date	ECN#	Originator	Description
**	12/12/2017	5991990	FCCL	Initial Release.

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