

信頼性試験成績書

0.18um Process Technology Product and Package Legacy Fujitsu Reliability Engineering Test Report

Wafer Process : Aizu Fujitsu
Assembly : J-Devices

Reliability Test 1

Device Type : MCU 1
Package Type : Plastic LQFP-100 Package

Test Item	Test Condition	Tested Number	Tested Time	Failed Number
High Temperature Operation Life 高温連続動作試験	125 °C V _{DD} =Maximum Rating	231 (77x3Lots)	1000h	0
Power Temperature Cycling	-40°C~125°C	45	(a) 1000cyc	0
High Temperature Storage 高温保存試験	150 °C	45	1000h	0
Temperature Cycling 温度サイクル	-65°C~150°C	231 (77x3Lots)	(a) 1000cyc	0
Unbiased HAST	130°C, 85%RH	231 (77x3Lots)	(a) 96h	0
HAST	110°C, 85%RH V _{DD} =Maximum Rating	231 (77x3Lots)	(a) 264h	0

(a) Pre-condition: Baking (125°C, 24h) +Moisture Absorption (30°C/60%RH, 192h) +IR 260°C Max. x3

Endurance + Data Retention Test

Device Type : MCU 2
Package Type : Plastic QFP-100 Package

Test Condition (Ta)	Tested Number	Failed Number
10Kcyc (105°C) + Data Retention Bake : 1008h (150°C)	117 (3Lots)	0
10Kcyc (105°C) + HTOL : 1000h (150°C)	114 (3Lots)	0

Document History Page

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**	5769784	KUMI	Initial release.