



**Please note that Cypress is an Infineon Technologies Company.**

The document following this cover page is marked as “Cypress” document as this is the company that originally developed the product. Please note that Infineon will continue to offer the product to new and existing customers as part of the Infineon product portfolio.

**Continuity of document content**

The fact that Infineon offers the following product as part of the Infineon product portfolio does not lead to any changes to this document. Future revisions will occur when appropriate, and any changes will be set out on the document history page.

**Continuity of ordering part numbers**

Infineon continues to support existing part numbers. Please continue to use the ordering part numbers listed in the datasheet for ordering.

## Objective

This example demonstrates an implementation of an LED breathing effect using PWMs and XOR gates without CPU involvement.

## Overview

The code example shows how to use the flexibility of a PSoC 4200DS device to implement a breathing LED effect exclusively in hardware without any CPU usage. The example uses two PWMs and an XOR gate to implement the design. A user switch is also used in the example to gate the breathing LED effect. Refer to the [Design](#) section for details on the implementation.

**Note:** This example is supported ONLY in PSoC 4200DS devices with at least two PWMs and one universal digital block (UDB) for implementing the XOR and AND gate logic functions.

## Requirements

**Tool:** PSoC Creator 4.1 or later

**Programming Language:** C (ARM® GCC 5.4.1)

**Associated Parts:** PSoC 4 parts with at least two PWMs and one UDB

**Related Hardware:** [CY8CKIT-146](#), [CY8CKIT-042](#), [CY8CKIT-044](#), [CY8CKIT-042-BLE](#), and [CY8CKIT-043](#)

## Design

### Breathing LED

When the intensity of an LED is gradually varied from zero to maximum and then from maximum to zero in a periodic fashion, a breathing LED effect is generated. This effect is analogous to the human breathing pattern – inhale (zero to max intensity) and exhale (max to zero intensity).

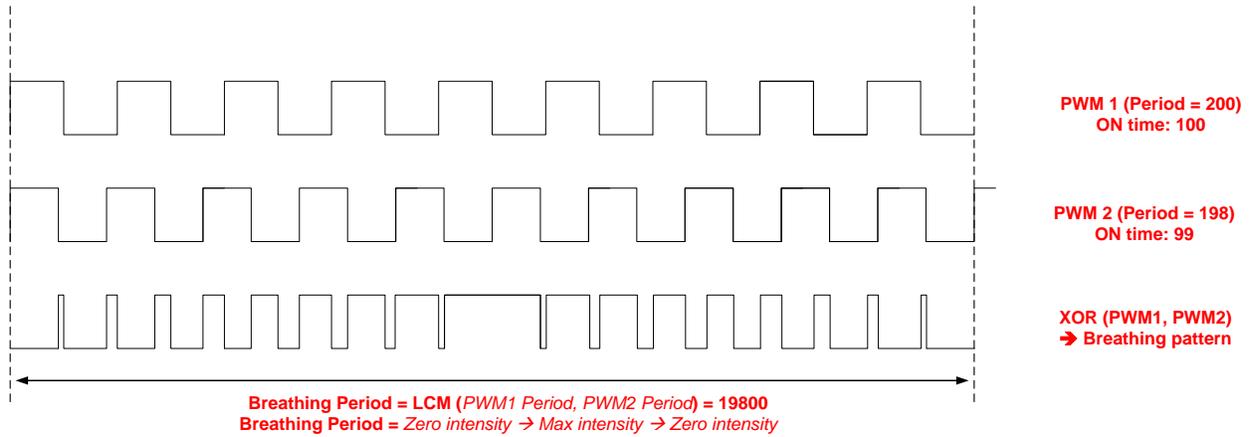
To create this breathing effect on the LED, a PWM signal whose duty cycle varies gradually from zero to max and vice-versa (shown in [Figure 1](#)) is required. The gradual increase and decrease in the duty cycle can be linear or exponential depending on the aesthetic preference. A method of generating a linearly varying PWM using PSoC is presented in this example. The implementation uses hardware blocks available in PSoC and does not involve any CPU usage. This PWM output can also be generated using a single PWM but with firmware controlling the duty cycle and period.

Figure 1: PWM to Generate a Breathing LED Pattern



The PWM waveform shown in Figure 1 can be easily generated in hardware using two PWMs and a simple XOR gate. Take two PWMs, one with period 200, another with period 198 and both having a duty cycle of 50%. XOR the two PWM outputs to generate a waveform that provides a breathing LED output, as shown in Figure 2.

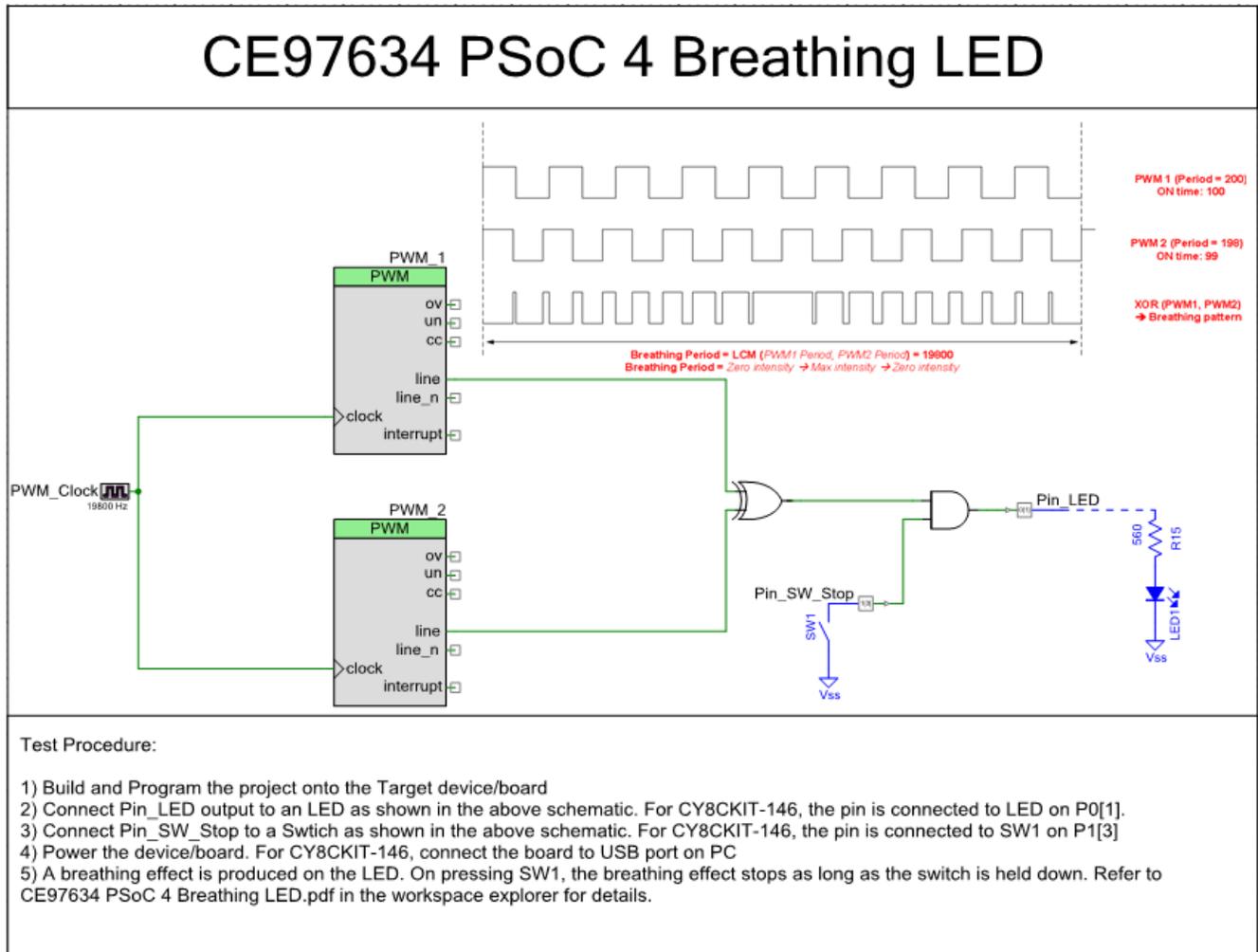
Figure 2: Generating Breathing PWM Output Using PWMs and XOR



As can be seen from the Figure 2, the breathing period of the LED is given by the least common multiple (LCM) of the two periods.

## PSoC 4 Implementation

Figure 3. Top Design Schematic of the Example



Firmware starts the PWM clock and the PWMs. After that, the CPU is put into sleep mode because it is not required for any other operations. This complete implementation is with respect to CY8CKIT-146. In order to use the CE for other kits one can refer [Table 1](#) for port mapping.

### Design Extensions

The example can be extended to generate any desired breathing effect by changing the breathing period and step size. The breathing period and step size at which the LED intensity is varied can be derived using the below relationship.

If  
 PWM1 Period → N clock cycles  
 Desired Step Size every N/2 cycles → x cycles;  $0 < x < N/4$   
 PWM1 ON time → N/2 cycles

Then,  
 PWM2 Period → N – 2x cycles,  
 PWM2 ON time → N/2 – x,  
 Breathing Period → LCM (N, N-2x) clock cycles

## Hardware Setup

No Additional Hardware setup is required.

## Software Setup

No Additional software setup is required.

## PSoC Creator Components

Table 1 lists the PSoC Creator Components used in this example, as well as the hardware resources used by each.

Table 1. List of PSoC Creator Components

Component	Version of the Components	Instance Name	Hardware Resources	Non-default Parameter settings
PWM (TCPWM Mode)	2.10	PWM_1	One TCPWM block	<b>Period:</b> 200 <b>Compare:</b> 100
		PWM_2	One TCPWM block	<b>Period:</b> 198 <b>Compare:</b> 99
Clock	2.20	PWM_Clock	One Clock Divider	<b>Frequency:</b> 19800 Hz <b>Use fractional divider:</b> <i>Checked</i>
XOR	1.0	-	Part of one UDB	None
AND	1.0	-	Part of one UDB	None
Digital Output Pin	2.20	Pin_LED	CY8CKIT-146 -> P0[1] CY8CKIT-043 -> P1[6] CY8CKIT-044 -> P0[6] CY8CKIT-042 -> P0[3]	None
Digital Input Pin	2.20	Pin_SW_Stop	CY8CKIT-146 -> P1[3] CY8CKIT-043 -> P0[7] CY8CKIT-044 -> P0[7] CY8CKIT-042 -> P0[7]	<b>Drive mode:</b> <i>Resistive pull up</i> <b>Initial drive state:</b> 1

## Design-Wide / Global Resources

Figure 4. Pin Tab for CY8CKIT-146 4200DS Prototyping board in Design Wide Resources (.cydwr file)

	Name	Port	Pin	Lock
<input type="checkbox"/>	Pin_LED	P0 [1]	20	<input checked="" type="checkbox"/>
<input type="checkbox"/>	Pin_SW_Stop	P1 [3]	4	<input checked="" type="checkbox"/>

Figure 5. Clock Tab Settings in CY8CKIT-146 Design-Wide Resources

Type	Name	Domain	Desired Frequency	Nominal Frequency	Accuracy (%)	Tolerance (%)	Divider	Start on Reset	Source Clock
System	EXTCLK	N/A	24 MHz	? MHz	±0	-	0	<input type="checkbox"/>	
System	Timer (WDT)	N/A	? MHz	? MHz	±0	-	32768	<input type="checkbox"/>	LFCLK
System	ILO	N/A	40 kHz	40 kHz	-50, +100	-	0	<input checked="" type="checkbox"/>	ILO
System	LFCLK	N/A	? MHz	40 kHz	-50, +100	-	0	<input checked="" type="checkbox"/>	ILO
System	HFCLK	N/A	24 MHz	24 MHz	±2	-	2	<input checked="" type="checkbox"/>	IMO
System	SYSCLK	N/A	? MHz	24 MHz	±2	-	1	<input checked="" type="checkbox"/>	HFCLK
System	IMO	N/A	48 MHz	48 MHz	±2	-	0	<input checked="" type="checkbox"/>	
Local	PWM_Clock	FF	19.8 kHz	19.8 kHz	±2	±5	1.212 4/32	<input type="checkbox"/>	Auto: HFCLK

## Operation

1. Build the example project by navigating to **Build > Build <Project Name>** in PSoC Creator.
2. Program the example to the device by navigating to **Debug > Program**.
3. The **User LED (LED1)** connected to **P0[1]** should start displaying the breathing effect.
4. Press the **User Switch (SW1)** connected to **P1[3]** and as long as it is held down, the LED will be OFF. On releasing the switch, the LED will start displaying the breathing effect again. Note that the PWM component is not stopped when the switch is pressed; only the LED output is gated.

## Upgrade Information

N/A

## Related Documents

Table 2 lists all relevant application notes, code examples, knowledge base articles, device datasheets, and component datasheets.

Table 2. Related Documents and Resources

Application Notes		
<a href="#">AN79953</a>	Getting Started with PSoC® 4	Provides details on getting started resources for PSoC 4
PSoC Creator Component Datasheets		
<a href="#">TCPWM</a>	PSoC 4 Timer Counter Pulse Width Modulator (TCPWM) component	
<a href="#">Digital Logic Gates</a>	Supports boolean operation with configurable number of inputs	
Device Documentation		
<a href="#">PSoC 4200DS Datasheets</a>		
<a href="#">PSoC 4200DS Technical Reference Manuals</a>		
Development Kits		
<a href="#">PSoC 4 Kits</a>		
Software		
<a href="#">PSoC Creator Training</a>		
<a href="#">PSoC 3/4/5 Code Examples</a>		
<a href="#">Video Library</a>		

## PSoC Resources

Cypress provides a wealth of data at [www.cypress.com](http://www.cypress.com) to help you to select the right PSoC device for your design, and quickly and effectively integrate the device into your design. For a comprehensive list of resources, see [KBA86521](#), [How to Design with PSoC 3](#), [PSoC 4](#), and [PSoC 5LP](#). Refer to [AN79953 - Getting Started with PSoC® 4](#) to get started with PSoC 4. The following is an abbreviated list of resources to get started with PSoC 4:

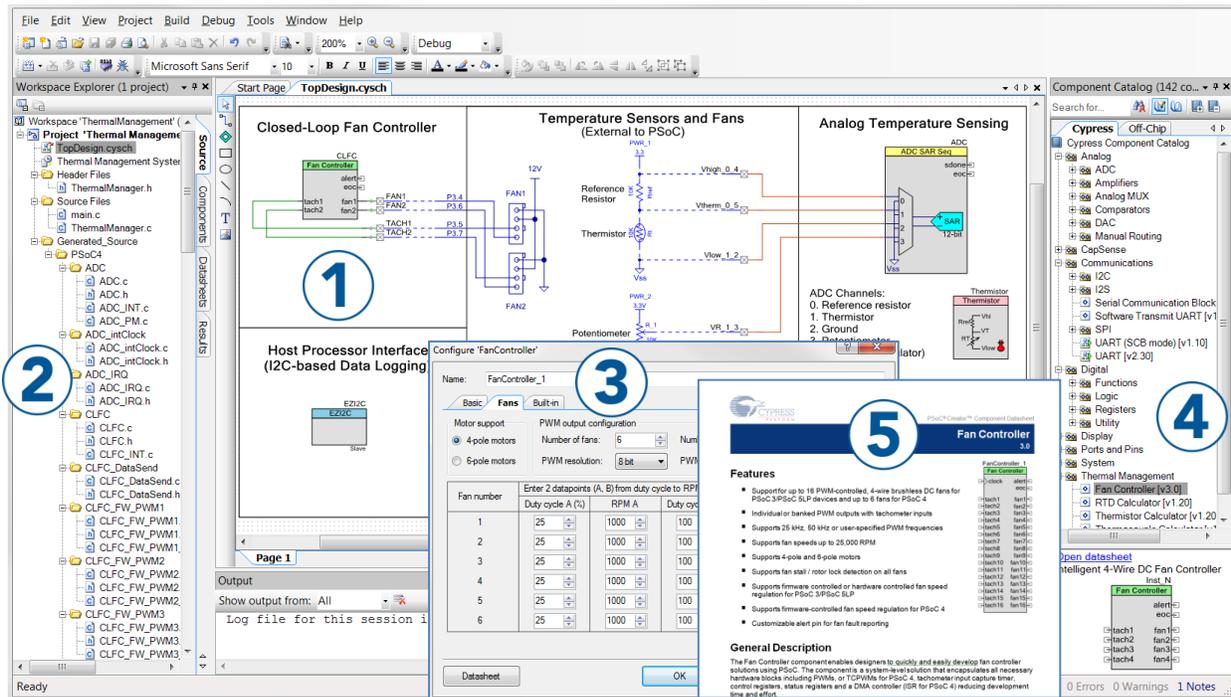
- **Overview:** [PSoC Portfolio](#), [PSoC Roadmap](#)
- **Product Selectors:** [PSoC 1](#), [PSoC 3](#), [PSoC 4](#), [PSoC 5LP](#). In addition, [PSoC Creator](#) includes a device selection tool.
- **Datasheets:** Describe and provide electrical specifications for the [PSoC 4 device family](#)
- **CapSense Design Guide:** Learn how to design capacitive touch-sensing applications with the PSoC 4 family of devices.
- **Application Notes and Code Examples:** Cover a broad range of topics, from basic to advanced level. Many of the application notes include code examples. Visit the [PSoC 3/4/5 Code Examples page](#) for a complete list of PSoC Creator code examples available across application notes, kits, and PSoC Creator.
- **PSoC Training Videos:** These videos provide step-by-step instructions on getting started building complex designs with PSoC.
- **Technical Reference Manuals (TRM):** Provide detailed descriptions of the architecture and registers in each PSoC 4 device family.
- **Development Kits:**
  - [CY8CKIT-040](#), [CY8CKIT-042](#), [CY8CKIT-044](#), [CY8CKIT-046](#), [CY8CKIT-041-40XX](#), and [CY8CKIT-041-41XX](#) PSoC 4 Kits, are easy-to-use and inexpensive development platforms. These kits include connectors for Arduino™ compatible shields and Digilent® Pmod™ daughter cards.
  - [CY8CKIT-049](#) and [CY8CKIT-043](#) are very low-cost prototyping platforms for sampling PSoC 4 devices.
  - [CY8CKIT-001](#) is a common development platform for all PSoC family devices.
- The [MiniProg3](#) device provides an interface for flash programming and debug. The same functionality is built into most kits through the KitProg present on-board.

## PSoC Creator

PSoC Creator is a free Windows-based Integrated Design Environment (IDE). It enables concurrent hardware and firmware design of systems based on PSoC 3, PSoC 4, PSoC 5LP, and PSoC 4200DS. See Figure 6 – with PSoC Creator, you can:

1. Drag and drop Components to build your hardware system design in the main design workspace
2. Codesign your application firmware with the PSoC hardware
3. Configure Components using configuration tools
4. Explore the library of 100+ Components
5. Review Component datasheets

Figure 6. PSoC Creator Features



The screenshot displays the PSoC Creator IDE interface with several key features highlighted by numbered callouts:

- 1:** A schematic diagram titled "Temperature Sensors and Fans (External to PSoC)" showing a "Closed-Loop Fan Controller" connected to a "Host Processor Interface (I2C-based Data Logging)" and an "Analog Temperature Sensing" block. The fan controller is connected to two fans (FAN1, FAN2) and a thermistor.
- 2:** The "Workspace Explorer" on the left side of the IDE, showing a project tree for "ThermalManagement" with various source files and components.
- 3:** The "Configure Fan Controller" dialog box, which allows users to set parameters for up to 6 fans, including motor support, PWM output configuration, and a table for fan duty cycle and RPM.
- 4:** The "Component Catalog" on the right side of the IDE, showing a search bar and a list of available components such as ADC, Amplifiers, and Registers.
- 5:** A "Fan Controller" component datasheet window, providing detailed features and a general description for the component.

Fan number	Enter 2 datapoints (A, B) from duty cycle to RPM	Duty cycle A (%)	RPM A	Duty cycle B (%)	RPM B
1		25	1000	100	100
2		25	1000	100	100
3		25	1000	100	100
4		25	1000	100	100
5		25	1000	100	100
6		25	1000	100	100

## Document History

Document Title: CE97634 - PSoC® 4 Breathing LED

Document Number: 001-97634

Revision	ECN	Orig. of Change	Submission Date	Description of Change
**	4777393	MSUR	05/26/2015	New spec.
*A	4795367	MSUR	06/12/2015	Minor text edits.
*B	5629922	NMIT	02/16/2017	<p>Updated Overview:  Updated description.  Updated Design:  Updated Breathing LED:  Updated description.  Updated PSoC 4 Implementation:  Updated Figure 3.  Updated Hardware Setup:  Updated Figure "Hardware Block Diagram for CY8CKIT-146 4200DS Prototyping Board".  Updated description.  Updated PSoC Creator Components:  Replaced "Components" with "PSoC Creator Components" in heading.  Updated Table 1.  Updated Design-Wide / Global Resources:  Updated Figure 4.  Updated Figure 5.  Updated Operation:  Updated description.  Updated Related Documents:  Updated Table 2.  Updated PSoC Resources:  Updated description.  Updated PSoC Creator:  Updated description.  Updated to new template.</p>
*C	5665948	NMIT	03/20/2017	<p>Updated Hardware Setup:  Removed Figure "Hardware Block Diagram for CY8CKIT-146 4200DS Prototyping Board".  Updated description.  Added Software Setup.  Updated to new template.  Completing Sunset Review.</p>

## Worldwide Sales and Design Support

Cypress maintains a worldwide network of offices, solution centers, manufacturer's representatives, and distributors. To find the office closest to you, visit us at [Cypress Locations](#).

### Products

ARM® Cortex® Microcontrollers	<a href="http://cypress.com/arm">cypress.com/arm</a>
Automotive	<a href="http://cypress.com/automotive">cypress.com/automotive</a>
Clocks & Buffers	<a href="http://cypress.com/clocks">cypress.com/clocks</a>
Interface	<a href="http://cypress.com/interface">cypress.com/interface</a>
Internet of Things	<a href="http://cypress.com/iot">cypress.com/iot</a>
Memory	<a href="http://cypress.com/memory">cypress.com/memory</a>
Microcontrollers	<a href="http://cypress.com/mcu">cypress.com/mcu</a>
PSoC	<a href="http://cypress.com/psoc">cypress.com/psoc</a>
Power Management ICs	<a href="http://cypress.com/pmic">cypress.com/pmic</a>
Touch Sensing	<a href="http://cypress.com/touch">cypress.com/touch</a>
USB Controllers	<a href="http://cypress.com/usb">cypress.com/usb</a>
Wireless Connectivity	<a href="http://cypress.com/wireless">cypress.com/wireless</a>

### PSoC® Solutions

[PSoC 1](#) | [PSoC 3](#) | [PSoC 4](#) | [PSoC 5LP](#)

### Cypress Developer Community

[Forums](#) | [WICED IOT Forums](#) | [Projects](#) | [Videos](#) | [Blogs](#) | [Training](#) | [Components](#)

### Technical Support

[cypress.com/support](http://cypress.com/support)

PSoC is a registered trademark and PSoC Creator is a trademark of Cypress Semiconductor Corporation.



Cypress Semiconductor  
198 Champion Court  
San Jose, CA 95134-1709

© Cypress Semiconductor Corporation, 2015-2017. This document is the property of Cypress Semiconductor Corporation and its subsidiaries, including Spansion LLC ("Cypress"). This document, including any software or firmware included or referenced in this document ("Software"), is owned by Cypress under the intellectual property laws and treaties of the United States and other countries worldwide. Cypress reserves all rights under such laws and treaties and does not, except as specifically stated in this paragraph, grant any license under its patents, copyrights, trademarks, or other intellectual property rights. If the Software is not accompanied by a license agreement and you do not otherwise have a written agreement with Cypress governing the use of the Software, then Cypress hereby grants you a personal, non-exclusive, nontransferable license (without the right to sublicense) (1) under its copyright rights in the Software (a) for Software provided in source code form, to modify and reproduce the Software solely for use with Cypress hardware products, only internally within your organization, and (b) to distribute the Software in binary code form externally to end users (either directly or indirectly through resellers and distributors), solely for use on Cypress hardware product units, and (2) under those claims of Cypress's patents that are infringed by the Software (as provided by Cypress, unmodified) to make, use, distribute, and import the Software solely for use with Cypress hardware products. Any other use, reproduction, modification, translation, or compilation of the Software is prohibited.

TO THE EXTENT PERMITTED BY APPLICABLE LAW, CYPRESS MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS DOCUMENT OR ANY SOFTWARE OR ACCOMPANYING HARDWARE, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. To the extent permitted by applicable law, Cypress reserves the right to make changes to this document without further notice. Cypress does not assume any liability arising out of the application or use of any product or circuit described in this document. Any information provided in this document, including any sample design information or programming code, is provided only for reference purposes. It is the responsibility of the user of this document to properly design, program, and test the functionality and safety of any application made of this information and any resulting product. Cypress products are not designed, intended, or authorized for use as critical components in systems designed or intended for the operation of weapons, weapons systems, nuclear installations, life-support devices or systems, other medical devices or systems (including resuscitation equipment and surgical implants), pollution control or hazardous substances management, or other uses where the failure of the device or system could cause personal injury, death, or property damage ("Unintended Uses"). A critical component is any component of a device or system whose failure to perform can be reasonably expected to cause the failure of the device or system, or to affect its safety or effectiveness. Cypress is not liable, in whole or in part, and you shall and hereby do release Cypress from any claim, damage, or other liability arising from or related to all Unintended Uses of Cypress products. You shall indemnify and hold Cypress harmless from and against all claims, costs, damages, and other liabilities, including claims for personal injury or death, arising from or related to any Unintended Uses of Cypress products.

Cypress, the Cypress logo, Spansion, the Spansion logo, and combinations thereof, WICED, PSoC, CapSense, EZ-USB, F-RAM, and Traveo are trademarks or registered trademarks of Cypress in the United States and other countries. For a more complete list of Cypress trademarks, visit [cypress.com](http://cypress.com). Other names and brands may be claimed as property of their respective owners.