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AN205179

F²MC-8FX Family MB95410H/470H Series 8-Bit Microcontroller One Phase Power Meter (CS5464) Solution Real Time Clock and Data Storage Operation

Associated Part Family: MB95410H/470H Series

This application note describes how to use One Phase Power Meter (CS5464) solution's real time clock and data storage function.

1 Introduction

This application note describes how to use One Phase Power Meter (CS5464) solution's real time clock and data storage function.

Section 2 explains the Background.

Section 3 explains the HW Diagram.

Section 4 explains the HW Reference SCH.

Section 5 explains the FW diagram.

Section 6 explains the FW Function List.

2 Background

Background of Real time Clock and Data Storage Function

2.1 Overview

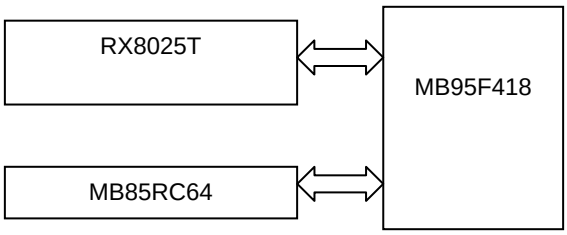
In the power meter solution, real time clock unit is RX8025T, and data storage unit is AT24C64. The communication method between MCU and these two units is I^2C communication.

3 HW Diagram

Hardware diagram of real time clock unit and data storage unit

3.1 HW Diagram of Real Time Clock Unit and Data Storage Unit

Figure 1. Hardware diagram



4 HW Reference SCH

Hardware reference SCH of real time clock unit and data storage unit

Figure 2. Real time Clock Unit

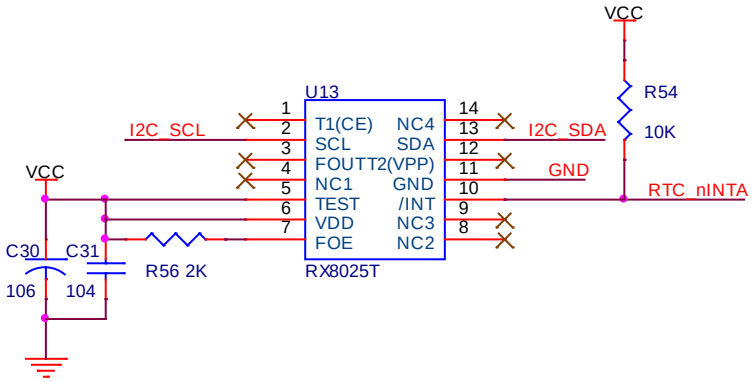
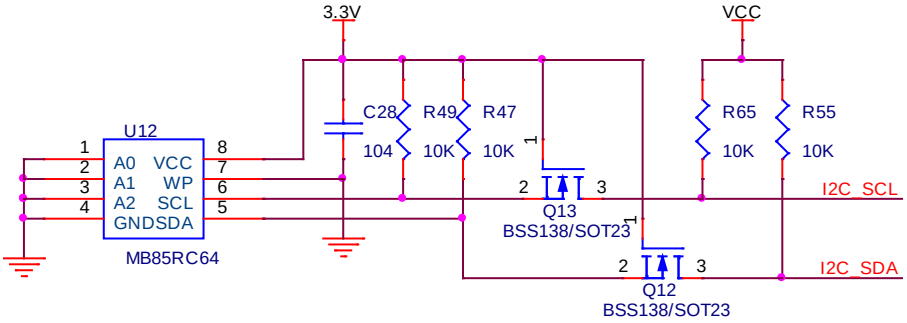


Figure 3. Data Storage Unit



5 FW diagram

Firmware system diagram

5.1 Real time Clock Unit Communication Protocol

For RTC function's communication protocol, refer to the IIC bus protocol which is shown in Figure 4.

Description is as below:

1. Master sends a start condition and a byte data including 7 bits slave address and 1 bit R/W.RTC's salve address is 0110010.
2. Master waits ACK from slave.
3. Master sends a byte RTC register address (0~F), which is shown in Figure 5.
4. Master waits ACK from slave.
5. Master sends n bytes data to slave. And at the end of each byte master needs to wait slave's ACK.
6. Master sends a stop condition to slave.

Figure 4. RTC Communication Protocol

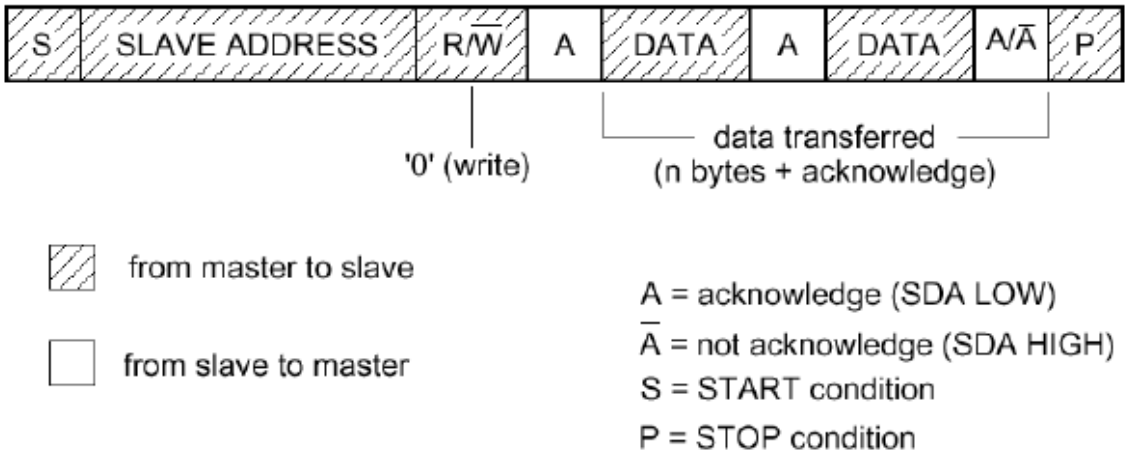


Figure 5. RTC's register Description

Address	Function
0	SEC
1	MIN
2	HOUR
3	WEEK
4	DAY
5	MONTH
6	YEAR
7	RAM
8	MIN Alarm
9	HOUR Alarm
A	WEEK Alarm DAY Alarm
B	Timer Counter 0
C	Timer Counter 1
D	Extension Register
E	Flag Register
F	Control Register

5.2 Data Storage Unit Communication Protocol

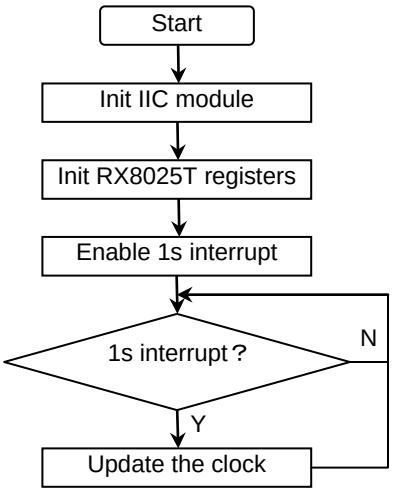
For EEPROM function's communication protocol, refer to the IIC bus protocol which is shown in Figure 4.

Description is as below:

1. Master sends a start condition and a byte data including 7 bits slave address and 1 bit R/W.EEPROM's salve address is 1010000.
2. Master waits ACK from slave.
3. Master sends 2 bytes EEPROM data address (0x0000~0xFFFF).First sends the address high byte. Then sends the address low byte. And in the end of each byte master needs to wait slave's ACK.
4. Master sends n bytes data to slave. And in the end of each byte master need to wait slave's ACK.
5. Master sends a stop condition to salve.

5.3 Firmware System Diagram

Figure 6: Firmware System Diagram



6 FW Function List

6.1 API

Table 1. FW API List

Function Prototype	Description
void RTC_Config(void)	Config working mode for RTC RX8025T
void RTC_IntDisable(void)	Disable interrupt output for RTC
void RTC_Read(void)	Read RTC RX8025T calendar data
void RTC_Write(void)	Set RTC RX8025T calendar data
void EEPROM_Write(INT8U *eeBuff)	Write a series of data byte to EEPROM
void EEPROM_Read(INT8U *eeBuff)	Read a series of data byte from EEPROM
INT8U EEPROM_Write_Verify(INT8U *eeBuff)	Write a series of data byte to EEPROM and verify the result
INT8U EEPROM_Read_Verify(INT8U *eeBuff)	Read a series of data byte from EEPROM and validate checksum

6.2 HAL

Table 2. FW HAL List

Function Prototype	Description
void IIC_Init(void)	Init IIC bus
INT8U IIC_Acknowledge(void)	waitting ACK from slave
INT8U IIC_Start(INT8U slave_address)	Master generates a START condition on IIC bus and transmit slave_address
INT8U IIC_Restart(INT8U slave_address)	Master generates a RESTART condition on IIC bus
INT8U IIC_Stop(void)	Master generates a STOP condition on IIC bus
INT8U IIC_SendByte(INT8U outDat)	Master send out a byte of data and return with ACK/NACK
INT8U IIC_Write(INT8U *buff, INT8U total)	Master write a string of bytes through IIC us
INT8U IIC_ReadByte(void)	Master read a byte of data and set ACK/NACK
INT8U IIC_Read(INT8U *buff, INT16U total)	Master read a string of data bytes through IIC us

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Document History

Document Title: AN205179 – F²MC-8FX Family MB95410H/470H Series 8-Bit Microcontroller One Phase Power Meter (CS5464) Solution Real Time Clock and Data Storage Operation

Document Number: 002-05179

Revision	ECN	Orig. of Change	Submission Date	Description of Change
**	-	HUAL	06/18/2011	Initial release
*A	5277409	HUAL	05/20/2016	Migrated Spansion Application Note MCU-AN-500119-E-10 to Cypress format.

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