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AN212

Migrating from FM24CL64 to FM24CL64B

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Associated Project: No

Associated Part Family: FM24CL64, FM24CL64B

Software Version: None

Related Documents: For a complete list, [click here](#)

AN212 discusses the key differences that need to be considered when migrating from FM24CL64 to FM24CL64B. FM24CL64 is now obsolete and this application note explains how FM24CL64B is a replacement for FM24CL64.

Introduction

FM24CL64B, a 64-Kbit I²C F-RAM™, is a replacement device for FM24CL64, which is now obsolete. The two devices are identical in terms of pinout, package composition and dimensions, read/write functionality, Write Protect operation, and address pin functionality. This application note discusses the key differences between the two devices that need to be considered when migrating from FM24CL64 to FM24CL64B.

Drop-In Replacement or Not?

For most designs, FM24CL64B is a drop-in replacement for FM24CL64. From a software point of view, the two devices are identical. From a hardware point of view, the key difference between the two devices is that the FM24CL64B has a slightly higher standby current. Additionally, FM24CL64B datasheet adds a power-up and power-down ramp rate specification of 30 μs / V and a power-up to first-access specification of 1 ms.

Table 1 shows the compatibility chart of FM24CL64 and FM24CL64B. For a detailed comparison, see Table 3.

Table 1. Compatibility Chart

FM24CL64 Feature or Spec	Is FM24CL64B compatible?
Package	Yes
Pinout	Yes
Temperature Range	Yes
Operating Voltage	Yes
Operating Current	Yes
Standby Current	No
Read / Write Function	Yes
Timing / Frequency	Yes
Data Retention	Refer to Table 3
Endurance	Yes

Ordering Part Numbers

Table 2 gives the recommended FM24CL64B ordering part numbers that correspond to the now obsolete FM24CL64 ordering part numbers

Table 2. Recommended Ordering Part Numbers for Migration

FM24CL64		FM24CL64B		Comments
Ordering Part Number	Status	Ordering Part Number	Status	
FM24CL64-G	Obsolete	FM24CL64B-G	In production	No hardware or software change is required.
FM24CL64-GTR		FM24CL64B-GTR		
FM24CL64-DG		FM24CL64B-DG		
FM24CL64-DGTR		FM24CL64B-DGTR		

Comparison of FM24CL64 and FM24CL64B

Table 3 gives a detailed comparison of the two devices.

Table 3. Detailed Comparison

	FM24CL64	FM24CL64B	Comments
Package Types	-G, -DG	-G, -DG	Identical “green” SOIC and TDFN packages
Package Outlines	SOIC-8, TDFN-8	SOIC-8, TDFN-8	Identical SOIC outline and board footprint
Pinout	-	-	Identical
Temperature Range	-40 °C to +85 °C	-40 °C to +85 °C	Identical
Operating Voltage Range	2.7 V to 3.65 V	2.7 V to 3.65 V	Identical
Active Supply Current	75 μ A @ 100 kHz 400 μ A @ 1 MHz	100 μ A @ 100 kHz 300 μ A @ 1 MHz	The FM24CL64B offers lower active current at 1 MHz
Standby Current	1 μ A (max)	6 μ A (max) 3 μ A (typical)	FM24CL64B has higher standby current
Read / Write Function	-	-	Identical 2-byte addressing, Identical Slave IDs, Identical Device Select bits
Clock Frequency	1 MHz	1 MHz	Identical
Data Retention	45 years (+85 °C)	10 years (+85 °C) 38 years (+75 °C) 151 years (+65 °C)	Data retention is lower
Endurance (Write/Read Cycles)	Unlimited	1E+14	FM24CL64B is virtually unlimited at 1 MHz (1700 years for a 64-byte loop)
V _{DD} Power-Up Ramp Rate (t _{VR})		30 μ s / V	Power-up ramp rate should be greater than 30 μ s / V for FM24CL64B
V _{DD} Power-Down Ramp Rate (t _{VF})	-	30 μ s / V	Power-down ramp rate should be greater than 30 μ s / V for FM24CL64B
Power-Up to First Access (t _{PU})	-	1 ms	After power-up, the first access of FM24CL64B should be after 1 ms

Critical Considerations

You should consider all the parameter differences mentioned in Table 3 during the migration to FM24CL64B. This section discusses the critical differences. System designers should also review the [datasheet](#) when migrating to the new part.

V_{DD} Ramp Rate

V_{DD} power-up and power-down ramp rate specifications are added in FM24CL64B device. Ensure that the power-up and power-down ramp rates are greater than 30 μ s / V in your system.

Power-Up to First Access

The power-up to first access specification is added in the FM24CL64B device. Ensure that the FM24CL64B device is accessed only after 1 ms from power-up.

Summary

AN212 discussed the differences between FM24CL64 and FM24CL64B that need to be considered during migration to the FM24CL64B. This application note demonstrates that FM24CL64B is a drop-in replacement for FM24CL64 for most designs.

Related Documents

Datasheet

[FM24CL64B: 64-Kbit \(8 K × 8\) Serial \(I²C\) F-RAM datasheet](#)

Document History

Document Title: Migrating from FM24CL64 to FM24CL64B - AN212

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Revision	ECN	Orig. of Change	Submission Date	Description of Change
**	3944550	GVCH	03/26/2013	New Spec
*A	4271353	MEDU	02/04/2014	Updated to Cypress Template Updated Active Supply Current for FM24CL64 from "70 μ A @ 100 kHz " to "75 μ A @ 100 kHz " Added data retention spec to FM24CL64B at 85 $^{\circ}$ C Updated Power-up to first Access for FM24CL64B from "10 ms" to "1 ms" Removed V_{IH} (max) spec from Table 2
*B	4498650	GVCH	09/10/2014	Changed title from "Differences between FM24CL64 and FM24CL64B" to "Migrating from FM24CL64 to FM24CL64B." Updated abstract. Added " Ordering Part Numbers " section. Added title for Table 3 . Added " Related Documents " section.

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