



# Release Notes

## CY3207-ISSP Production Programmer

February 2, 2011

Thank you for your interest in the CY3207-In-System Serial Programming (ISSP) Programmer. This document lists installation requirements and describes software and hardware updates and changes.

### System Requirements and Recommendations

Refer to the ISSP Programmer Ver1.999 Release Notes for system requirements.

ISSP Programmer Ver1.999 Release Notes are available in the kit CD or can be downloaded from <http://www.cypress.com/CY3207-ISSP>.

### Installation

To install the ISSP Programmer Ver1.999, download the latest version from the website or use the CD provided with the kit. To install from CD, insert the kit CD into the CD-ROM drive. If the installer does not start automatically, start it manually by executing *Setup.exe* in the root directory of the CD. Follow the onscreen instructions to complete installation. For more information on installation, see the user guide.

**Note** ISSP software must be installed and run in Windows 2000 compatibility mode in Windows Vista/7. For instructions regarding Windows 2000 compatibility mode, please see the ISSP Programmer User Guide, which may be found on the Kit CD or at <http://www.cypress.com/CY3207-ISSP>.

### Updates

Cypress recommends checking for updated versions of ISSP Programmer before installing the software contained on the kit CD. Check <http://www.cypress.com/CY3207-ISSP> for the latest downloads of kit documentation.

### New for Revision \*A

The CY3207-ISSP Programmer programs PSoC<sup>®</sup> 1 ICs with hex files created with the PSoC Designer<sup>™</sup> software. The development environment and material included with the kit are as follows.

Kit Version: Build version \*A

Material included in the kit

ISSP Programmer	ISSP Programmer Ver1.999, programs a HEX file into a PSoC device
Main board hardware	Rev. F
Power Supply	9-V DC supply
USB cable	PC to Programmer connection.
Programming cable	Programmer to target board 5-pin connector.
Documentation	ISSP Guide (on kit CD)

The CY3207 hardware revision has added level-4 ESD protection to programming lines. Devices supported by CY3207-ISSP Programmer are detailed in the software release notes for ISSP Programmer.



## Limitations

The CY3207 programmer will only support one open instance of the ISSP Programmer. Delays and freezing is seen if more than one instance is opened.

## Known Defects

Defect	Work Around
1. CY3207-ISSP kit does not contain signed drivers. When installing ISSP Programmer software, a pop-up window will appear indicating that the driver is not signed	Click <b>Continue anyway</b> and proceed with installation. For Windows 7 installation, see the ISSP Programmer User Guide for details.
2. While in reset (XRES held high), if AC52 key is pushed to ISSP bus, CY8C20X36A/46A/66A/96A parts enter into device test mode. In this mode, internal regulator of the chip is disabled and the core of the controller is powered at a higher voltage than specified, thus damaging the device.	To solve this problem, avoid the CY8C20X36A/46A/66A/96A parts from seeing the AC52 key in reset condition. This is possible if ISSP lines of CY8C20X36A/46A/66A/96A are not shared. Cypress does not recommend sharing ISSP bus of CY8C20X36A/46A/66A/96A parts with other PSoC devices.
3. The ISSP Programmer cannot program pod chips, which are meant to emulate a family of parts, because only production parts are programmable by the CY3207-ISSP kit.	The recommended configuration is to use the blue CAT5 cable and ICE-Cube (CY3215-DK), MiniProg1 or MiniProg3 with PSoC Programmer to program any POD chips.
4. The CY3207-ISSP Programmer supplies power to the PSoC device only during socket programming.	This feature is available with the MiniProg1, MiniProg3, and CY3215-DK programmers, where the programmer can supply 5-V supply to a target board.
5. Programming in In-System Vdd Rising Edge and In-System Vdd Stable On modes pass when the IC is placed on the ZIP socket of ISSP Programmer, instead of the target board.	Because programming lines are shared between ZIP socket and RJ45 connector, programming is successful if PSoC is available on either end of the unit. Ensure that the device is placed on the target board before initiating in-system methods.



## Documentation

The CY3207-ISSP user guide is available on the kit CD.

For more information on in-system serial programming, refer to application notes [AN2014 Design for In-System Serial Programming \(ISSP\)](#) and [AN2026a In-System Serial Programming \(ISSP\) Protocol](#).

You can access the latest version of the silicon errata by visiting <http://www.cypress.com/psoc> and navigating to Errata.

## Technical Support

For assistance, go to <http://www.cypress.com/go/support> or contact our customer support at +1 (800) 541-4736 Ext. 8 (in the USA), or +1 (408) 943-2600 Ext. 8 (International).



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