



FW can poll port pin P1\_2 (VBUS SENSE) to detect a connection to a USB host. For polling to function correctly, pin P1\_2 drive mode must be set by FW to high impedance digital, and the P1\_2 input buffer must be configured by FW for LVTTTL input thresholds.

Attach negative lead of Type K thermocouple to P3.

PCB: PDCR-9511  
 PCA: 121R-51100

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