



CY8CKIT-002 PSoC® MiniProg3 Program and Debug Kit Release Notes

Release Date: February 1, 2018

Thank you for your interest in the CY8CKIT-002 PSoC® MiniProg3 Program and Debug Kit. This document lists kit contents, installation requirements, kit documentation, limitations, and known issues.

Kit Contents

The CY8CKIT-002 PSoC MiniProg3 Program and Debug Kit box includes the following:

- MiniProg3 programmer/debugger
- 10-pin ribbon cable
- USB Standard-A to Mini-B cable
- Quick Start Guide

Software and Tools

Download and install [PSoC Programmer](#). Follow the onscreen instructions to install the software.

Kit Revision

This is Rev. *C of the CY8CKIT-002 PSoC MiniProg3 Program and Debug Kit. In this revision, MiniProg3 firmware has been updated to support programming of the PSoC 6 MCU family of devices.

Limitations and Known Issues

The following are the limitations and known issues in this revision of the CY8CKIT-002 PSoC MiniProg3 Program and Debug Kit. These issues will be resolved in future revisions of this kit.

- **Issue:** Attaching MiniProg3 to a running PSoC 3/PSoC 5LP target device using the 10-pin cable causes a device reset.
Workaround: Connect the cable alone to the target board and then connect the other end to MiniProg3.
- **Issue:** Reset Mode programming fails if the VTARG pin of MiniProg3 is not connected to the target board VCC/VDD powering the target device.
Workaround: Power the MiniProg3 VTARG pin from PSoC Programmer by clicking the “Toggle Power” button after setting the correct voltage and proceed with programming the device.
- **Issue:** MiniProg3 Rev. *B or older revisions do not support programming the eFuses of PSoC 6 device family.
Workaround: Support for programming the eFuses of PSoC 6 device family will be available in the future revision of the MiniProg3.
- **Issue:** Device operations (Program, Verify, Erase, Checksum, and so on) fail for PSoC 6 MCU and FM0+ devices when using MiniProg3 at clock speeds of above 12 MHz.
Workaround: Disconnect the MiniProg3 from PC and reconnect it. Now, use a clock speed of below 12 MHz.
- **Issue:** Device operations (Program, Verify, Erase, Checksum, and so on) fail on FM0-V48-S6E1A1 when using MiniProg3, and if the operations are performed immediately after the Toggle Power operation in PSoC Programmer.
Workaround: Wait for four seconds after power is turned ON before executing device operations.



Documentation

The kit documents are available on the CY8CKIT-002 MiniProg3 Program and Debug Kit webpage (www.cypress.com/CY8CKIT-002).

Technical Support

For assistance, go to www.cypress.com/support.

Additional Information

For more information about PSoC Programmer and supported hardware, visit the PSoC Programmer webpage: www.cypress.com/psocprogrammer.



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