



PSoC® Programmer Release Notes

Version 3.26.0

June 2, 2017

PSoC Programmer is Cypress's programming toolchain used for programming various Cypress devices.

- Supports applications including: PSoC Creator™, PSoC Designer™, TrueTouch® Host Emulator and MTK, CyClockWizard, and Ez-Click.
- Supports all PSoC architectures including PSoC 1, PSoC 3, PSoC 4, PSoC 5LP, PSoC 6, TrueTouch, CapSense, and Clock devices.
- Supports all Cypress programming hardware such as MiniProg1, MiniProg3, TrueTouch Bridge, KitProg1, KitProg2, ICE-Cube, CY3240 USB-I2C Bridge.
- Provides a COM layer that can be used to create custom applications.
- Installs secondary applications such as Bridge Control Panel and Clock Programmer.

PSoC Programmer 3.26.0 release delivers:

- Pre-Production support of PSoC 60xx/61xx/62xx/63xx Families
- Support of new devices of CY8C4xxx, CY8C4xxx-BLE, CY8C4xxx-L, CY8C5xxxLP, CY8C40xx-S, CY8C41xx-S, CY8C44xx and CYPD2xx families
- Support of CY8C42xx-D device family
- Support of new TrueTouch families
- KitProg2 FW update with pre-production support of CY8CKIT-062-BLE PSoC 6 BLE and CY8CKIT-062 PSoC 6 Pioneer kits
- KitProg2 FW update with production support of CY8CKIT-146 PSoC 4200DS Prototyping kit.

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New Features for PSoC Programmer

PSoC Programmer 3.26.0 provides pre-production support for the PSoC 6 device family. It also features firmware updates for KitProg2 to support CY8CKIT-062-BLE and CY8CKIT-062 kits and Minipro3 firmware update to support the new PSoC 6 device family.

The following products are also delivered with this PSoC Programmer 3.26.0 release:

Product	Version
Bridge Control Panel	1.16.0
Clock Programmer	1.6.0
KitProg1	2.18
KitProg2	1.03
Minipro3	2.05 [3.09/2.09]
TrueTouch Bridge	1.35

Pre-Production support of PSoC 6 Family

PSoC Programmer 3.26.0 provides the programming and debugging support for Cypress's latest PSoC 6 device family via both PSoC Programmer and PSoC Creator. It supports programming and debugging of PSoC 6 devices via SWD and JTAG interfaces. Additionally, it provides support for the following programming hardware: MiniPro3, KitProg2, ULinkPro and CMSIS-DAP to program the PSoC 6 device on the PSoC 6 BLE and PSoC 6 Pioneer kits.

KitProg2 update with pre-production support of CY8CKIT-062-BLE and CY8CKIT-062 kits

KitProg2 1.03 features programming and debugging support of CY8CKIT-062-BLE PSoC 6 BLE and CY8CKIT-062 PSoC 6 Pioneer kits. It supports programming and debugging of the PSoC 6 device via Cypress' proprietary programming, the CMSIS-DAP, and Drag-n-Drop programming via mass-storage interfaces. It also features the bridging interface to communicate with the target device using I2C, SPI and UART communication protocols. KitProg2 also supports Fine Voltage Control, to enable software control of the operating voltage of the target device.

KitProg2 update with production support of CY8CKIT-146 PSoC 4200DS Prototyping kit

KitProg2 1.03 features programming and debugging support of CY8CKIT-146 PSoC 4200D Prototyping kit. It supports programming and debugging of the PSoC 4200DS device via Cypress' proprietary programming and the CMSIS-DAP, and Drag-n-Drop programming via mass-storage interfaces. It also features the bridging interface to communicate with the target device using I2C and UART communication protocols.

Minipro3 firmware update with support of PSoC 6 Family

Minipro3 2.05 [3.09/2.09] provides the programming support of the PSoC 6 and PSoC 6 BLE devices via SWD and JTAG interface.

Bridge Control Panel

Bridge Control Panel 1.16.0 adds the support of SPI interface for communicating to the PSoC 6 BLE and PSoC 6 Pioneer kits.

Clock Programmer

Clock Programmer 1.6.0 is updated to address various defects in this release.



KitProg1 firmware update

KitProg1 firmware is updated to address various defects in this release.

New Devices in PSoC Programmer

The following new devices have been added to PSoC Programmer version 3.26.0

Family	Device
CY8C4xxx	CY8C4245PVA-452Z
	CY8C4245PVS-452Z
CY8C4xxx-BLE	CY8C4247FNI-BL473
CY8C4xxx-L	CY8C4248BZI-L469
CY8C5xxxLP	CY8C5266FNI-LP205
CY8C40xx-S	CY8C4024LQA-S411
	CY8C4024PVA-S411
	CY8C4024PVA-S412
	CY8C4025LQA-S411
	CY8C4025PVA-S411
	CY8C4025PVA-S412
	CY8C4045LQA-S411
	CY8C4045PVA-S411
	CY8C4045PVA-S412
	CY8C4024LQS-S411
	CY8C4024PVS-S411
	CY8C4024PVS-S412
	CY8C4025LQS-S411
	CY8C4025PVS-S411
	CY8C4025PVS-S412
	CY8C4045LQS-S411
	CY8C4045PVS-S411
	CY8C4045PVS-S412
	CY8C4045PVS-S412
CY8C41xx-S	CY8C4126AZI-S423
	CY8C4126AZI-S433
	CY8C4145AZI-S423
	CY8C4124PVA-S422
	CY8C4125PVA-S422
	CY8C4124PVS-S422
	CY8C4125PVS-S422
	CY8C4124PVE-S422
	CY8C4125PVE-S422
CY8C42xx-DS	CY8C4245PVI-DS402
	CY8C4245FNI-DS402
	CY8C4246PVI-DS402
	CY8C4246FNI-DS402
	CY8C4046FNI-DS402
	CY8C4046PVI-DS402
	CY8C4045FNI-DS402
	CY8C4045PVI-DS402



Family	Device
CY8C44xx	CY8C4A45FNQ-483
	CY8C4A45LQQ-483
CY8C60xx	CY8C6036BZI-F04
	CY8C6016BZI-F04
CY8C61xx	CY8C6116BZI-F54
	CY8C6136BZI-F14
	CY8C6136BZI-F34
	CY8C6137BZI-F14
	CY8C6137BZI-F34
	CY8C6137BZI-F54
	CY8C6117BZI-F34
CY8C62xx	CY8C6246BZI-D04
	CY8C6247BZI-D44
	CY8C6247BZI-D34
	CY8C6247BZI-D54
	CY8C637BZI-MD76
CY8C63xx	CY8C6336BZI-BLF03
	CY8C6316BZI-BLF03
	CY8C6316BZI-BLF53
	CY8C6336BZI-BLD13
	CY8C6347BZI-BLD43
	CY8C6347BZI-BLD33
	CY8C6347BZI-BLD53
	CY8C6347FMI-BLD13
	CY8C6347FMI-BLD43
	CY8C6347FMI-BLD33
	CY8C6347FMI-BLD53
	CY8C637BZI-BLD74
	CY8C637FMI-BLD73
	CY8C68237BZ-BLE
	CY8C68237FM-BLE
CY8CTMA6xx	CY8CTMA616LTI-12
	CY8CTMA616LTI-13
	CY8CTMA616AA-13
	CY8CTMA616AA-13ES
	CY8CTMA616AE-13
	CY8CTMA616AE-13ES
	CY8CTMA616AA-12
	CY8CTMA616AE-12
	CY8CTMA616AA-22
	CY8CTMA616AA-23
	CY8CTMA616AA-23ES
	CY8CTMA616AE-22
CY8CTMA616AE-23	
CYAT61xxx	CYAT61652-56LWA41
	CYAT61652-56LWS41
	CYAT61658-56LWA41
	CYAT61658-56LWS41
	CYAT61659-56LWA41



Family	Device
	CYAT61659-56LWS41
	CYAT61659-56LWS41ES
CYAT71xxx	CYAT71610-56LWA41
	CYAT71610-56LWS41
	CYAT71612-56LWA41
	CYAT71612-56LWS41
	CYAT71615-56LWA41
	CYAT71615-56LWS41
	CYAT71618-56LWA41
	CYAT71618-56LWS41
	CYAT71618-56LWS41ES
	CYAT71630-56LWA41
	CYAT71630-56LWS41
	CYAT71632-56LWA41
	CYAT71632-56LWS41
	CYAT71635-56LWA41
	CYAT71635-56LWS41
	CYAT71638-56LWA41
	CYAT71638-56LWS41
	CYAT71638-56LWS41ES
CYAT81xxx	CYAT81653-100AA48
	CYAT81653-100AS48
	CYAT81656-100AA48
	CYAT81656-100AS48
	CYAT81659-100AA48
	CYAT81659-100AS48
CYAT82xxx	CYAT82687-100AA31
	CYAT82687-100AS31
	CYAT82687-100AA39
	CYAT82687-100AS39
	CYAT82687-100AA46
	CYAT82687-100AA54
	CYAT82687-100AA54
	CYAT82687-100AS54
CYPD2xxx	CYPD2192-24LQXA
	CYPD2192-24LQXAT
	CYPD2192-24LQXS
	CYPD2192-24LQXST
	CYPD2194-24LQXA
	CYPD2194-24LQXAT
	CYPD2194-24LQXS
	CYPD2194-24LQXST

Known Issues

The following is a list of known issues for PSoC Programmer.

Defect	Description	Tracking ID#
All JTAG operations and SWD Read/Verify operations for	Please contact Cypress support if you experience this	263858

Defect	Description	Tracking ID#
PSoC 6 silicon takes longer than expected. It also true for Read operation via KitProg2 in CMSIS-DAP mode.	problem.	
DAP_ReadIO API in command line interface returns failed status.	Please contact Cypress support if you experience this problem. As a workaround, use swdior and swdiow operations.	264004
PSoC Programmer can't acquire PSoC 6 device in test mode for programming operations if selected SWD/JTAG clock is less than 3.0 MHz	Please contact Cypress support if you experience this problem. As a workaround, use 3.0 MHz as minimum SWD/JTAG clock for PSoC 6 operations in PSoC Programmer.	263813
When using 3 rd party IDEs (e.g. IAR EW), it is not possible to program PSoC 6 silicon with Dual-core or CM4-core project if User Flash is empty.	Use a FlashLoader for the Cortex-M0+ (set the device name to CY8C6xx_CM0p) to program a complete flash image (M0+ and M4 code). Then set the device name to CY8C6x7_CM4 and attach debugger to the Cortex-M4. Alternatively, program PSoC 6 silicon in PSoC Programmer with miniProg3. Then you can program in 3 rd party IDE.	259219
MiniProg3 firmware upgrade may not complete successfully if MiniProg3 remains connected after reinstall of PSoC Programmer	To prevent, disconnect the MiniProg3 from the PC before you first open newer version of PSoC Programmer. Ensure PSoC Programmer reports '2.05 [3.09/2.09]' firmware version of the connected MiniProg3 <div style="border: 1px solid black; padding: 2px; width: fit-content;"> Results MiniProg3 version 2.05 [3.09/2.09] </div> If PSoC Programmer reports an error about loading firmware or if it reports a different firmware version (e.g., [3.09/2.08]), just unplug and re-plug the MiniProg3.	244588
KitProg2 USBUART driver is not supported on Windows XP and Vista.	KitProg2 USBUART driver is not supported on Windows XP and Vista (x32 and x64). This does not allow using UART functionality of KitProg2 (programming operations and I2C works). The problem will be fixed in the next release of PSoC Programmer.	250159
Device operations fails for FM0+ Kits, if performed right after Power On	Device operation (Program/Verify/Erase/Checksum) fails for FM0+ Kits, connected via MiniProg3, if performed right after "Toggle Power" operation. To avoid this, please wait for four seconds after power is toggled before doing further steps.	251941
Erase Block does not always save changes after hitting Enter	When using "Erase Block" function in Programmer GUI, you can notice that Block ID and Bank ID fields may not be saved between dialog sessions. That is a known problem of MaskedTextBox component of WinForms .NET library. In order to save these	183455

Defect	Description	Tracking ID#
	values between dialog sessions, follow the below mentioned steps: <ol style="list-style-type: none"> 1) Delete existing value in Block ID/Bank ID field; 2) Enter new value; 3) Press Enter or press Erase Block Button. 	
Firmware upgrade of KitProg kits (CY8CKIT-040/042)	When starting KitProg's Firmware upgrade in PSoC Programmer GUI, make sure that Bootloader Host (Creator's) tool is not running. Otherwise upgrade may fail due to attempts of Host tool to access KitProg's bootloader.	178919
Abort button does not work for MiniProg3 in Power Cycle mode (SWD protocol)	Abort does not work for MiniProg3 in Power Cycle mode for SWD protocol. Workaround here is just to re-plug MiniProg3 from USB-port during this lengthy operation.	193660
MiniProg3 overrides Reset button on CY8CKIT-001	MiniProg3 keeps XRES pin in active HIGH or LOW state after ISSP operation is complete. So it will block using the Reset button if active XRES state of target is HIGH (and MiniProg3 is connected to ISSP header).	194179
Python examples fail with new Python interpreters	Due to changes in Python from version 2.6 to 3.0, Python code examples fail. One case is the print statement being replaced by the print() function. A syntax error is thrown when attempting to run the examples with Python 3.0+. We are currently requesting users to use Python 2.6 for our code examples.	169474
CY3240 Bridge always keeps INT line in the low state	This causes a problem if the Bridge's INT line is connected to the target's XRES line with active LOW polarity. Due to HW limitation, the INT line can't be moved to High-Z state from FW. Design your boards respectively for compatibility with the CY3240 bridge.	187785
Miniprogram v1.77 causes an acquire failure.	In previous releases of the MiniProg1 firmware, the reset line was pulled low during the power cycle programming. MiniProg1 now supports devices that have active low reset line states. To account for this, MiniProg1 now leaves the reset line in a high-z state. Be aware of any pull-up or pull-down resistor circuitry that could hold the chip in a reset state.	69058
PRoC UI devices cannot be programmed above 3.3V	<p>The MiniProg3 and MiniProg1 programmers can supply power to the target device in excess of 3.3 V. The PRoC UI devices can only be programmed in the 1.7-3.3V range. Programming above 3.3V may cause damage to the PRoC-UI device or the radio chip.</p> <p>Warnings have been added to PSoC Programmer if the user were to select the PRoC-UI devices in a hazardous configuration.</p>	N/A
BCP runs out-of-memory if	Bridge Control Panel cannot collect data on the	185014

Defect	Description	Tracking ID#
monitoring data for a long time	chart (or in the table) for a long time, otherwise it will run out of memory. In such situations, you need to restart the GUI. It is recommended that you fix the Chart's samples, so it will scroll. Set the scroll window size in "Scroll" parameter of "Char > Variable Settings" menu (for example, 1000-10000)	
Programmer does not work if installed on another disk	Customers who install Programmer into a customer directory must not use '-' characters in the file path name.	138786

Known Limitations

The following are the known limitations with PSoC Programmer:

- JTAG/SWD communication for PSoC3/5 is not stable with the clock above 3.2 MHz. The maximum programming speed available is dependent upon the CFGSPEED NVL setting from the previous programming session. This setting has two options 12MHz and 48MHz. This configures how fast the IMO runs, which in turn configures how fast the BUS Clock can run. For reliable results, Programming/Debugging should not be done faster than 1/3 of the BUS Clock. The higher the clock speed used, the more likely a failure will occur. The length of the cable as well as capacitance on the board can affect the ability to successfully send data over JTAG or SWD. For PSoC3/5 flash read/write operations, recommended upper frequency bound is 3.0-3.2 MHz.
- 3rd party Integrated Development Environments (e.g. µVision IDE – Keil and IAR Embedded Workbench - IAR Systems) do not support programming if silicon or flash rows are in protected state. If your device is flashed with PSoC Creator's project, where "Chip Protection" option is not set to "Open" or/and "Flash Security" settings page contains at least one protected row, please do "Erase All Flash" operation in PSoC Programmer before using the device in 3rd party IDE.
- In the Bridge Control Panel, executing several commands simultaneously does not guarantee that these commands will be executed one by one without any delay. Each command is a separate USB transaction, a delay depends on the PC characteristics and the connected bridge type (more significant for USB HID devices such as KitProg and KitProg2).
- Upgrading the KitProg1/KitProg2 firmware in a virtual environment (e.g. "Parallels Desktop" or "VMWare Fusion") may result in fail during initialization or detection of the Kit as a "Bootloader device" instead of "KitProg" after firmware update.

This is because during a firmware update process, KitProg changes the mode from "KitProg" to "Bootloader" and then back to "KitProg". This changing of the VID/PID of the USB device causes the virtual machine to unplug the Kit from the guest OS.

To prevent this issue, use one of the following solutions:

- a) Configure the virtual machine to automatically connect all USB devices to the guest OS.
- b) If automatic connection of all USB devices is not supported, the virtual machine may be configured to connect automatically only the devices with known VID/PID. For the KitProg, switch the Kit into the three different modes successively ("Bootloader", "KitProg" and "Mass Storage") and configure the virtual machine to connect the detected devices (in each mode) to the guest OS automatically.
- c) If the virtual machine does not support such configuration, you need to manually reconnect the Kit after switching from KitProg to Bootloader mode or vice versa.

- While PSoC Creator supports the superset programming concept, PSoC Programmer does not. It is intended to be a production programming tool rather than prototyping. After the firmware project is complete, produce the HEX file for the corresponding part number and program it in production with PSoC Programmer or third party tools.
- The supported programming and bridging hardware can only be used by one application at a time. Closing the port in one application releases the hardware for other client applications.
- There is no programming support for wafer sale parts.
- The specific locations are not indicated in case of programming verification fails.
- The name of CMSIS-DAP port of FM0+ kits may change on different PC and USB ports. This is reproducible using FM0+ kits with empty Serial Number string (USB Device Descriptor) or if it is not unique. In this case, OS generates a unique string based on physical USB port address and this string is used in PSoC Programmer for creation of unique name for CMSIS-DAP port. Please take this into account when developing your own applications, which are using port acquisition APIs, exposed in PSoC Programmer.
- ICE4000 is no longer supported in PSoC Programmer.
- When using the ICE-Cube or MiniProg1 for programming, PSoC Programmer applies 3.3 V to the XRES pin during connection. This may cause power to be applied to the target system. During programming, 3.3 V is applied to the target system's SCLK(P1-1), SDATA (P1-0), and XRES pins.
- The MiniProg1 programmer does not support the CY8C25/26xxx parts. The ICE-LPT and ICE-4000 programmers support the CY8C25/26xxx parts. If required, use PSoC Programmer version 2.33 or earlier.
- CY3210-MiniProg1 may have two capacitors soldered to the SCL and SDA programming lines causing failures during programming. To remove these capacitors, contact Cypress technical support for additional steps in addressing this issue.
- The CY3240 USB-I2C Bridge firmware cannot be upgraded in the field using the Bootloader interface. If an upgrade is required, you need to reprogram its firmware completely from *usbtoiiic.hex* located in the PSoC Programmer installation folder. For reprogramming, you need to use any PSoC 1 programmer (such as MiniProg1 or MiniProg3) connected to a 5-pin ISSP header on the Bridge's board. Select "Power Cycle" for programming the Bridge. Cypress recommends customers to upgrade to the superset MiniProg3 kit, which should replace the CY3240 Bridge in field.
- PSoC Programmer should not be used with FM0+ devices on Serial Programming Mode. Serial Programming Mode is assumed to be used with serial interface such as UART or USB. It is not assumed to be used with SWD interface used by PSoC Programmer.

Installation

Minimum and Recommended Requirements

Hardware/Operation System Requirements	Minimum	Recommended
Processor Speed	2 GHz	2 GHz Dual Core
GB of RAM	2 GB	3 GB
GB of free hard drive space	1 GB	1 GB
Screen resolution	1024x768	1280x1024
CD/DVD Drive	Not Req.	✓ *
USB	Full Speed	2.0 Hi-Speed
Windows® XP (SP3 or higher), Vista, Windows 7 / 8 / 8.1 / 10	✓	✓
Software Prerequisites **	Minimum/Recommended Version	
Microsoft Internet Explorer	7	
.NET Framework	2.0 SP2	
Adobe Reader (for viewing PDF Documentation)	6	9+
Windows Installer	3.1	
Python – For Code Examples	2.6	2.6

* CD/DVD drive is only required for installation with no web access.

** Software prerequisites are checked/installed by Programmer's CyInstaller (except Python interpreters).

Applications Dependent on a PSoC Programmer Installation

The following applications require PSoC Programmer to be preinstalled. All Cypress software and kit products, which use PSoC Programmer, install it as well (minimum required version):

- PSoC Designer
- PSoC Creator
- TrueTouch Host Emulator
- MTK
- Ez-Click
- CyClockWizard

The following applications are included in the PSoC Programmer installation:

- Bridge Control Panel (mandatory)
- Clock Programmer (mandatory)
- USB and I²C PSoC 1 Bootloader Hosts (optional, by default included)
- Examples (optional, by default included)



Update Instructions

As part of the installation process, the Cypress Update Manager utility is also installed and located on the **Start** menu under the Cypress folder. You can use this utility to update all the programs you installed when updates for these become available.

Follow the instructions provided by the CyInstaller.

Check for the software updates to the Cypress PSoC development tools on the following web pages:

PSoC Software Tool	Link
PSoC Designer	http://www.cypress.com/go/psocdesigner
PSoC Creator	http://www.cypress.com/go/psoccreator
PSoC Programmer	http://www.cypress.com/go/psocprogrammer

Installation Notes

The installation process is a set of wizards that walk you through installing various components. You can install PSoC Programmer and various prerequisites from the web, or from a CD. There are slight differences in the process, based on the medium used to install the software. CyInstaller is supported by both the web installation and through an ISO image that can be downloaded and burned to a CD.

The CDs provide the necessary prerequisites and the wizards to guide you through installing the appropriate software. The web installation requires you to download and install the executables separately. The following sections contain more specific installation details.

Note Do not plug in any programming hardware until all the software installation is complete.

Web Installation

If you are downloading the software from the web, you should run the PSoC Programmer executable.

1. Double-click the PSoC Programmer executable file to launch the PSoC Programmer InstallShield Wizard.
2. Install all the prerequisites as needed.
3. Follow the prompts to install PSoC Programmer. The CyInstaller for PSoC Programmer opens and displays a series of steps to install PSoC Programmer and various drivers. When complete, close the installer.

Note that installation may fail when using the web due to firewall or administrator privileges. Contact your IT support for assistance or download the ISO image provided on the Programmer web page and burn the image and install Programmer from the CD.

PSoC Programmer CD Installation

The PSoC Programmer ISO image contains PSoC Programmer and various prerequisites.

1. Burn and load the CD into the PC. The main installer program should run automatically. If not, double-click the *cyautorun.exe* file to launch it.
2. On the main installer, click the **Install PSoC Programmer [version]...** button to launch the PSoC Creator InstallShield Wizard.
3. Follow the prompts in the wizard. The first step prompts to install PSoC Programmer.
The CyInstaller for PSoC Programmer opens and displays a series of steps to install PSoC Programmer and various drivers.

4. Continue to follow the prompts until PSoC Programmer and the drivers are installed, and then resume with the main installer program.

Cypress PSoC Kit CD Installation

A kit CD contains PSoC Programmer and additional applications, such as PSoC Creator or PSoC Designer, documentation, and prerequisites needed for the associated kit. The installation process is similar to the PSoC Programmer CD installation; however, the overall process differs, as follows:

1. Load the CD. The kit installer program should run automatically. If not, double-click the autorun program to launch it.
2. On the kit installer, follow the prompts to begin the installation process. The first step prompts to install the PSoC Programmer.
3. The CyInstaller for PSoC Programmer opens and displays a series of steps to install PSoC Programmer and various drivers.
4. Continue to follow the prompts until PSoC Programmer and the drivers are installed, then resume with the kit installer program.
5. Continue the prompts to install the application IDEs, PSoC Creator or PSoC Designer. Refer to the respective release notes for these tools for detailed instructions.

Device Driver Re-Installation

Drivers for all Cypress devices are installed along with PSoC Programmer. Drivers are removed from the system during uninstallation of PSoC Programmer.

If you need to re-install drivers manually, do the following:

- Navigate to the PSoC Programmer root installation directory.
- Open the *Drivers* folder and run *driverui.bat* to uninstall current drivers.
- Run the *driver.bat* file. This will install drivers from this PSoC Programmer release.

Coexistence with Older PSoC Programmer Releases

Only one version of PSoC Programmer can be installed in the system. During the installation of new PSoC Programmer version the previous one is removed. If you have an older version of PSoC Programmer (3.06 or below), uninstall it first and then proceed with installation of the latest release.

Further Reading

Documentation

Documentation is available in the PSoC Programmer root directory and under **Documents**. The documents include:

- Help files (CHM) for: PSoC Programmer GUI, PSoC-UI Programmer, HexToSvf
- PSoC Programmer COM Interface Guide
- PSoC Programmer Command Line Interface Guide
- PSoC Programmer Example Code
- Clock Programmer User Guide
- MiniProg3 User Guide



- Third-Party Tools User Guide. This user guide provides information on using the Cypress silicon in third party tools. It is located at `./3rd_party_configuration_files/Documents`

The Bridge Control Panel includes the following documents:

- Help File (CHM)
- I2C-USB Bridge Guide
- Example User Guide

Silicon Errata

The latest versions of the silicon errata are available on the website at <http://www.cypress.com/psoc> under **Related Documentation**.



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