Cypress Roadmap: Platform PSoC®

Q1 2015
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<th>PSoc® 1 Portfolio</th>
<th>M8C Core</th>
<th>24 MHz</th>
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### Performance Analog
- **CY8C29xxx**
  - 32K/2K
  - 64 GPIOs
  - CapSense, 16x Prog. Digital Blocks
  - 4x CMP, 1x14-bit ΔΣ ADC, 12x SC/CT PAB

- **CY8C27xxx**
  - 32K/2K
  - 44 GPIOs
  - CapSense, 8x Prog. Digital Blocks
  - 4x CMP, 1x14-bit ΔΣ ADC, 12x SC/CT PAB

- **CY8C28xxx**
  - 16K/1K
  - 12x Prog. Digital Blocks
  - 4x CMP, 1x14-bit ΔΣ ADC, 16x SC/CT PAB

### Programmable Digital
- **CY8C21x34**
  - 8K/0.5K
  - 28 GPIOs
  - CapSense, 4x Prog. Digital Blocks
  - 2x CMP, 1x10-bit ADC, 4x SC/CT PAB

- **CY8C23x33**
  - 8K/0.25K
  - 26 GPIOs
  - CapSense, 4x Prog. Digital Blocks
  - 1x CMP, 1x 8-bit SAR ADC, 4x SC/CT PAB

- **CY8C24x93**
  - 32K/2K
  - 36 GPIOs
  - 2x CMP

- **CY8C24x94**
  - 16K/1K
  - 56 GPIOs
  - CapSense, 4x Prog. Digital Blocks
  - 2x CMP, 2x14-bit SAR ADC, 6x SC/CT PAB

### Intelligent Analog
- **CY8C21x23**
  - 4K/0.25K
  - 16 GPIOs
  - 4x Prog. Digital Blocks
  - 2x CMP, 1x10-bit ADC

- **CY8C22x45**
  - 16K/1K
  - 38 GPIOs
  - CapSense, 8x Prog. Digital Blocks
  - 4x CMP, 1x10-bit SAR ADC, 6x SC/CT PAB

- **CY8C24x23**
  - 4K/0.25K
  - 24 GPIOs
  - CapSense, 4x Prog. Digital Blocks
  - 2x CMP, 1x10-bit ΔΣ ADC, 6x SC/CT PAB

### PSoc® 1 Portfolio
- **CY8C22x45**
  - 16K/1K
  - 38 GPIOs
  - CapSense, 8x Prog. Digital Blocks
  - 4x CMP, 1x10-bit SAR ADC, 6x SC/CT PAB

- **CY8C24x94**
  - 16K/1K
  - 56 GPIOs
  - CapSense, 4x Prog. Digital Blocks
  - 2x CMP, 2x14-bit SAR ADC, 6x SC/CT PAB

- **CY8C24x23**
  - 4K/0.25K
  - 24 GPIOs
  - CapSense, 4x Prog. Digital Blocks
  - 2x CMP, 1x10-bit ΔΣ ADC, 6x SC/CT PAB

- **CY8C23x33**
  - 8K/0.25K
  - 26 GPIOs
  - CapSense, 4x Prog. Digital Blocks
  - 1x CMP, 1x 8-bit SAR ADC, 4x SC/CT PAB

- **CY8C21x34**
  - 8K/0.5K
  - 28 GPIOs
  - CapSense, 4x Prog. Digital Blocks
  - 2x CMP, 1x10-bit ADC

### Integration
- **Flash KB/SRAM KB**
- **Comparator**
- **General-purpose input output pins**
- **Programmable digital block**
- **Analog-to-digital converter (incremental, successive approximation (SAR) or Delta-Sigma (ΔΣ))**
- **Switched capacitor/continuous time programmable analog block**

---

1. Flash KB/SRAM KB
2. General-purpose input output pins
3. Programmable digital block
4. Comparator
5. Analog-to-digital converter (incremental, successive approximation (SAR) or Delta-Sigma (ΔΣ))
6. Switched capacitor/continuous time programmable analog block
# PSoC® 3 Portfolio

**8051 | CapSense® | DMA | LCD | RTC | 4x Timer/Counter/PWM**

## Programmable Digital PSoC 3200

| Analog: ΔΣ ADC¹, 1x DAC², 2x CMP³, 0.9% Vref |
| Interfaces: FF⁴ I²C |

## Intelligent Analog PSoC 3400

| Analog: ΔΣ ADC¹, 2x DAC², 4x CMP³, 2x Opamps, 2x SC/CT PAB³, 0.9% Vref |
| Interfaces: FF⁴ I²C |

## Performance Analog PSoC 3600

| Analog: ΔΣ ADC¹, 2x/4x DAC², 0x/2x/4x CMP³, 0x/2x/4x Opamps, 0x/2x/4x SC/CT PAB³, 0.1% Vref |
| Interfaces: USB, FF⁴ I²C |

## Precision Analog PSoC 3800

| Analog: ΔΣ ADC¹, 2x/4x DAC², 0x/2x/4x CMP³, 0x/2x/4x Opamps, 0x/2x/4x SC/CT PAB³, 0.1% Vref |
| Interfaces: USB, FF⁴ I²C |

### CY8C3666

- 67 MHz, 64K/8K/2K⁶
- 0x/1x DFB⁷, 12b ADC¹
- 20x/24x UDB⁸, CAN⁹

### CY8C3866

- 67 MHz, 64K/8K/2K⁶
- DFB⁷, 20b ADC¹
- 20x/24x UDB⁸, CAN⁹, 72-CSP¹⁰

### CY8C3665

- 67 MHz, 32K/4K/1K⁶
- 0x/1x DFB⁷, 12b ADC¹
- 16x20x UDB⁸, 72-CSP¹⁰

### CY8C3865

- 67 MHz, 32K/4K/1K⁶
- 0x/1x DFB⁷, 20b ADC¹
- 16x20x UDB⁸

---

1. **ΔΣ** analog-to-digital converter
2. Fixed function
3. Comparator
4. Switched capacitor/continuous time programmable analog block
5. Digital filter block
6. Flash KB/SRAM KB/EEPROM KB
7. Controller area network
8. Chip scale package
9. Controller area network
10. Chip scale package

**Status**

- **Production**
- **Sampling**
- **Development**
- **Concept**

**Availability**

- **Q0YY**
- **Q0YY**
- **Q0YY**
- **Q0YY**
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### Intelligent Analog

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<td>Timer, counter, PWM block</td>
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### Programmable Digital

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### Programmable Analog

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<td>Programmable Analog</td>
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</table>

#### Integration

- 1 Flash KB/SRAM KB
- 2 Comparator
- 3 Analog-to-digital converter
- 4 Serial communication block programmable as I²C/SPI/UART
- 5 Current-output digital-to-analog converter
- 6 Timer, counter, PWM block
- 7 Bluetooth Low Energy
- 8 Universal Digital Block
- 9 Controller Area Network

#### Availability

- Production
- Sampling
- Development
- Concept

**Note:**
- CY8C44x6
  - 48 MHz, 64K/16K
  - Concept Only
  - Contact Sales

**Status:**
- QQYY

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**Source:**
- Owner: JFMD
- BUM: GOPA
### Applications
- MCU and discrete analog replacement
- User interface for button replacement
- User interface for heating, ventilation, air conditioning

### Features
- **32-bit MCU subsystem**
  - 16-MHz ARM® Cortex™-M0 CPU
  - Up to 16KB flash and 2KB SRAM
- **Programmable analog**
  - Two IDACs1 (7-bit and 8-bit), digitally controlled current source
  - One comparator (CMP)
- **CapSense® with SmartSense™ Auto-tuning**
  - One Cypress Capacitive Sigma-Delta™ (CSD) controller
  - Capacitive sensing supported on up to 16 pins
- **Programmable digital**
  - One configurable 16-bit timer, counter or pulse-width modulator (TCPWM) block
  - One I²C master or slave
- **Packages**
  - 8-pin SOIC, 16-pin SOIC, 16-QFN, 24-pin QFN

### Collateral
- Datasheet: [PSoC 4000](#)
- Technical Reference Manual: [PSoC 4000](#)

### Availability
- Sampling: Now
- Production: Now

---

1 Current-output digital-to-analog converter
**Applications**

User interface for home appliances
Digital and analog sensor hub
MCU and discrete analog replacement

**Features**

32-bit MCU Subsystem
24-MHz ARM® Cortex™-M0 CPU
Up to 32KB flash and 4KB SRAM

CapSense® with SmartSense™ Auto-tuning
Cypress Capacitive Sigma-Delta™ (CSD) controller
CapSense supported on up to 36 pins

Programmable Analog Blocks
Two comparators (CMPS)
Two opamps, programmed as PGAs, CMPs, filters, etc.
One 12-bit, 1-Msps SAR\(^1\) ADC
Two IDACs\(^2\) (2x 8-bit, 2x 7-bit)

Programmable Digital Blocks
Four programmable 16-bit TCPWM\(^3\) blocks
Two SCBs\(^4\): I\(^2\)C master or slave, SPI master or slave, or UART

Packages: 28-pin SSOP, 40-pin QFN, 44-pin TQFP, 48-pin LQFP

**Collateral**

Datasheet: PSoC 4 (CY8C4100)
Technical Reference Manual: PSoC 4

**Availability**

Sampling: Now
Production: Now

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1 Successive approximation register
2 Current-output digital-to-analog converter
3 Timer, counter, PWM block
4 Serial communication block programmable as I\(^2\)C/SPI/UART
PSoc® 4200
Programmable Digital Family

Applications
User interface for home appliances
Digital and analog sensor hub
MCU and discrete analog replacement

Features

32-bit MCU Subsystem
48-MHz ARM® Cortex™-M0 CPU
Up to 32KB flash and 4KB SRAM

CapSense® with SmartSense™ Auto-tuning
Cypress Capacitive Sigma-Delta™ (CSD) controller
CapSense supported on up to 36 pins

Programmable Analog Blocks
Two comparators (CMPs)
Two opamps, programmed as PGAs, CMPs, filters, etc.
One 12-bit, 1-Msps SAR\(^1\) ADC
Two IDACs\(^2\) (2x 8-bit, 2x 7-bit)

Programmable Digital Blocks
Four Universal Digital Blocks (UDBs): custom digital peripherals
Four programmable 16-bit TCPWM\(^3\) blocks
Two SCBs\(^4\): \(^I^2C\) master or slave, \(^SPI\) master or slave, or UART

Packages: 28-pin SSOP, 40-pin QFN, 44-pin TQFP, 48-pin LQFP

Collateral
Datasheet: PSoC 4 (CY8C4200)
Technical Reference Manual: PSoC 4

Availability
Sampling: Now
Production: Now

1 Successive approximation register
2 Current-output digital-to-analog converter
3 Timer, counter, PWM block
4 Serial communication block programmable as \(^I^2C/SPI/UART\)
PSoC® 4100 BLE-Series
Intelligent Analog Family with Bluetooth Low Energy

Applications
Sports and fitness monitors, wearable electronics, medical devices, home automation solutions, game controllers, sensor-based low-power systems for the Internet of Things (IoT)

Features
32-bit MCU subsystem
24-MHz ARM® Cortex™-M0 CPU
Up to 256KB flash and 32KB SRAM
Programmable AFE
Four opamps, configurable as PGAs, comparators, filters, etc.
One 12-bit, 1-Msps SAR ADC
CapSense® with SmartSense™ Auto-tuning
One Cypress Capacitive Sigma-Delta™ (CSD) controller with touchpad capability
Programmable digital logic
Four configurable TCPWM blocks: 16-bit timer, counter or PWM
Two configurable serial communication blocks (SCBs):
I²C master or slave, SPI master or slave, or UART
Packages
56-pin QFN, 68-pin CSP
Bluetooth Smart connectivity with Bluetooth 4.1
2.4-GHz BLE radio with integrated Balun

Collateral
Datasheet: PSoC 4 BLE (CY8C4XX7 BLE)

Availability
Sampling: Now
Production: 128KB Now, 256KB Q2 2015

Notes:
1 Analog front end(s)
2 Successive approximation register
3 Timer, counter, PWM block
4 Serial communication block programmable as I²C/SPI/UART
**PSoc® 4200 BLE-Series**
Programmable Digital Family with Bluetooth Low Energy

### Applications
Sports and fitness monitors, wearable electronics, medical devices, home automation solutions, game controllers, sensor-based low-power systems for the Internet of Things (IoT)

### Features

- **32-bit MCU subsystem**
  48-MHz ARM® Cortex™-M0 CPU
  Up to 256KB flash and 32KB SRAM

- **Programmable AFE**
  Four opamps, configurable as PGAs, comparators, filters, etc.
  One 12-bit, 1-Msps SAR ADC

- **CapSense® with SmartSense™ Auto-tuning**
  One Cypress Capacitive Sigma-Delta™ (CSD) controller with touchpad capability

- **Programmable digital logic**
  Four Universal Digital Blocks (UDBs): custom digital peripherals
  Four configurable TCPWM blocks: 16-bit timer, counter or PWM
  Two configurable serial communication blocks (SCBs):
  I²C master or slave, SPI master or slave, or UART

- **Packages**
  56-pin QFN, 68-pin CSP

- **Bluetooth Smart connectivity with Bluetooth 4.1**
  2.4-GHz BLE radio with integrated Balun

### Collateral

Datasheet: [PSoc 4 BLE (CY8C4XX7 BLE)]

### Availability

- **Sampling:** Now
- **Production:** 128KB Now, 256KB Q2 2015

---

1. Analog front end(s)
2. Successive approximation register
3. Timer, counter, PWM block
4. Serial communication block programmable as I²C/SPI/UART
**Applications**
User interface and host processor for home appliances
Digital and analog sensor hub
MCU and discrete analog replacement

**Features**

**32-bit MCU Subsystem**
24-MHz ARM® Cortex®-M0 CPU with a DMA controller and RTC\(^1\)
Up to 128KB flash and 16KB SRAM
Up to 55 GPIOs supporting analog, digital and CapSense interfaces

**CapSense® With SmartSense™ Auto-tuning**
Cypress Capacitive Sigma-Delta™ (CSD) controller

**Programmable Analog Blocks**
Six comparators (CMP)
Four opamps, programmable as PGAs, CMPs, filters, etc.
One 12-bit, 1-Msps SAR\(^2\) ADC
Four IDACs\(^3\) (2x 8-bit, 2x 7-bit)

**Programmable Digital Blocks**
Eight programmable 16-bit TCPWM\(^4\) blocks
Four SCBs\(^5\): I²C master or slave, SPI master or slave, or UART

**Two Controller Area Network (CAN) Controllers**

**Packages:** 48-pin LQFP, 64-pin TQFP (0.8-mm pitch),
64-pin TQFP (0.5-mm pitch), 68-pin QFN

**Collateral**
Datasheet: PSoC 4100M datasheet

**Availability**
Sampling: Now
Production: Q2 2015

---

1. Real-time clock
2. Successive approximation register
3. Current-output digital-to-analog converter
4. Timer, counter, PWM block
5. Serial communication block programmable as I²C/SPI/UART
**PSoC® 4200 M-Series**

**Programmable Digital Family**

**Applications**
- User interface and host processor for home appliances
- Digital and analog sensor hub
- LED control and communication for lighting systems

**Features**

**32-bit MCU Subsystem**
- 48-MHz ARM® Cortex®-M0 CPU with a DMA controller and RTC\(^1\)
- Up to 128KB flash and 16KB SRAM
- Up to 55 GPIOs supporting analog, digital and CapSense interfaces

**CapSense® With SmartSense™ Auto-tuning**
- Cypress Capacitive Sigma-Delta™ (CSD) controller

**Programmable Analog Blocks**
- Six comparators (CMPS)
- Four opamps, programmable as PGAs, CMPs, filters, etc.
- One 12-bit, 1-Mmps SAR\(^2\) ADC
- Four IDACs\(^3\) (2x 8-bit, 2x 7-bit)

**Programmable Digital Blocks**
- Four Universal Digital Blocks (UDBs): custom digital peripherals
- Eight programmable 16-bit TCPWM\(^3\) blocks
- Four SCBs\(^4\): I\(^2\)C master or slave, SPI master or slave, or UART

**Two Controller Area Network (CAN) Controllers**

**Packages**: 48-pin LQFP, 64-pin TQFP (0.8-mm pitch), 64-pin TQFP (0.5-mm pitch), 68-pin QFN

**Collateral**

Datasheet: [PSoC 4200M datasheet](#)

**Availability**

Sampling: Now
Production: Q2 2015

---

\(1\) Real-time clock  
\(2\) Successive approximation register  
\(3\) Current-output digital-to-analog converter  
\(4\) Timer, counter, PWM block  
\(5\) Serial communication block programmable as I\(^2\)C/SPI/UART

---

Owner: JFMD  
BUM: GOPA  
Platform PSoC Roadmap
**Applications**

User interface and host processor for home appliances  
Digital and analog sensor hub  
MCU and discrete analog replacement  
LED control and communication for lighting systems

**Features**

**32-bit MCU Subsystem**  
48-MHz **ARM® Cortex®-M0** CPU with a DMA controller and RTC  
Up to 256KB flash and 32KB SRAM  
Up to 98 GPIOs supporting analog and digital interfaces  
**CapSense® With SmartSense™ Auto-tuning**  
Two Cypress Capacitive Sigma-Delta™ (CSD) controllers  

**Programmable Analog Blocks**  
Two comparators (CMPs)  
Four opamps, configurable as PGAs, CMPs, filters, etc.  
One 12-bit, 1-Msps SAR ADC  
Four IDACs (2x 8-bit, 2x 7-bit)  

**Programmable Digital Blocks**  
Eight Universal Digital Blocks (UDBs): custom digital peripherals  
Eight configurable 16-bit TCPWM blocks  
Four SCBs: I2C master or slave, SPI master or slave, or UART  

**Full-Speed USB 2.0 Controller and Transceiver**  
Two Controller Area Network (CAN) Controllers  

**Packages:** 48-pin TQFP, 64-pin TQFP, 68-pin QFN, 124-pin µBGA

**Collateral**

Preliminary Datasheet: [Contact Sales](#)  

**Availability**

Sampling: Q3 2015  
Production: Q4 2015

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1. Real-time clock  
2. Successive approximation register  
3. Current-output digital-to-analog converter  
4. Timer, counter, PWM block  
5. Serial communication block programmable as I2C/SPI/UART
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<td><strong>PSOC 5400</strong></td>
<td><strong>PSOC 5600</strong></td>
<td><strong>PSOC 5800</strong></td>
</tr>
<tr>
<td>Analog: 1x ADC, 1x DAC, 2x CMP, 0.9% Vref</td>
<td>Analog: 1x ADC, 2x DAC, 4x CMP, 2x Opamps, 2x SC/CT PAB, 0.9% Vref</td>
<td>Analog: 2x ADC, 4x DAC, 4x CMP, 4x Opamps, DFB, 4x SC/CT PAB, 0.9% Vref</td>
<td>Analog: 2x/3x ADC, 4x DAC, 4x CMP, 4x Opamps, DFB, 4x SC/CT PAB, 0.1% Vref</td>
</tr>
</tbody>
</table>

**NEW**

- **CY8C5288**
  - 80 MHz, 256K/64K/2K
  - 12b SAR ADC
  - 24x UDB, 99-CSP

- **CY8C5268**
  - 67 MHz, 256K/64K/2K
  - 12b SAR ADC
  - 24x UDB

- **CY8C5267**
  - 67 MHz, 128K/32K/2K
  - 12b SAR ADC
  - 24x UDB

- **CY8C5266**
  - 67 MHz, 64K/16K/2K
  - 12b SAR ADC
  - 20x UDB

**NEW**

- **CY8C5488**
  - 80 MHz, 256K/64K/2K
  - 12b SAR ADC
  - 24x UDB, 99-CSP

- **CY8C5468**
  - 67 MHz, 256K/64K/2K
  - 12b SAR ADC
  - 24x UDB

- **CY8C5467**
  - 67 MHz, 128K/32K/2K
  - 12b SAR ADC
  - 24x UDB

- **CY8C5466**
  - 67 MHz, 64K/16K/2K
  - 12b SAR ADC
  - 20x UDB

**NEW**

- **CY8C5688**
  - 80 MHz, 256K/64K/2K
  - 2x 12b SAR ADC
  - 24x UDB, 99-CSP

- **CY8C5668**
  - 67 MHz, 256K/64K/2K
  - 12b ∆Σ ADC, 12b SAR/2x 12b SAR ADC
  - 24x UDB

- **CY8C5667**
  - 67 MHz, 128K/32K/2K
  - 12b ∆Σ ADC, 12b SAR/2x 12b SAR ADC
  - 24x UDB

- **CY8C5666**
  - 67 MHz, 64K/16K/2K
  - 12b ∆Σ ADC, 12b SAR ADC
  - 20x UDB

**NEW**

- **CY8C5888**
  - 80 MHz, 256K/64K/2K
  - 20b ∆Σ ADC, 2x 12b SAR ADC
  - 24x UDB

- **CY8C5868**
  - 67 MHz, 256K/64K/2K
  - 20b ∆Σ ADC, 2x 12b SAR ADC
  - 24x UDB

- **CY8C5867**
  - 67 MHz, 128K/32K/2K
  - 20b ∆Σ ADC, 12b SAR ADC
  - 24x UDB

- **CY8C5866**
  - 67 MHz, 64K/16K/2K
  - 20b ∆Σ ADC, 12b SAR ADC
  - 20x UDB

---

1. Analog-to-digital converter
2. Digital-to-analog converter
3. Comparator
4. Fixed function
5. Switched capacitor/continuous time programmable analog block
6. Digital filter block
7. Controller area network
8. Flash KB/SRAM KB/EEMROM KB
9. Universal digital block
10. Chip scale package
11. Delta-Sigma ADC
12. Production
13. Sampling
14. Development
15. Concept
16. Availability

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**ARM® Cortex™-M3 | CapSense® | DMA | LCD | RTC | 4x Timer/Counter/PWM**
# PSoC Platform Packages

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<th>Package</th>
<th>LQFP</th>
<th>PDIP</th>
<th>QFN</th>
<th>SOIC</th>
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